



RRB JE | SSE 2023

Foundation Batch

Analog Electronics

Day-3

> LIVE

1 PM

LAWRENCE Sir



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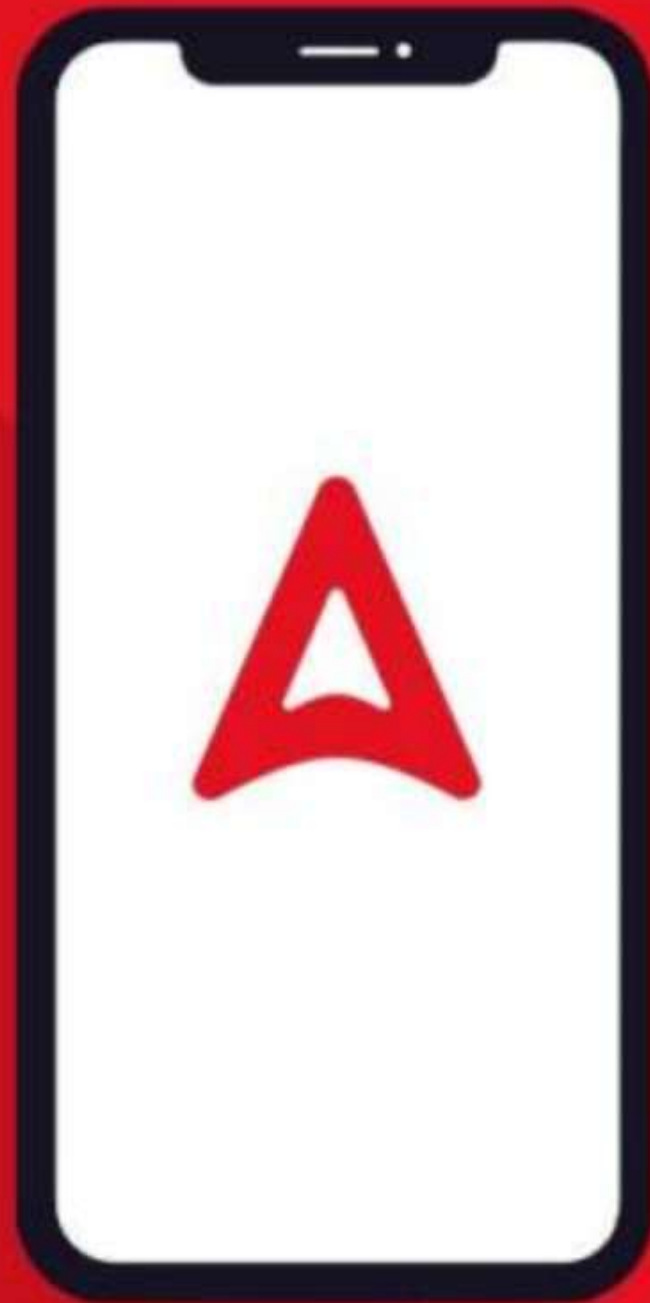
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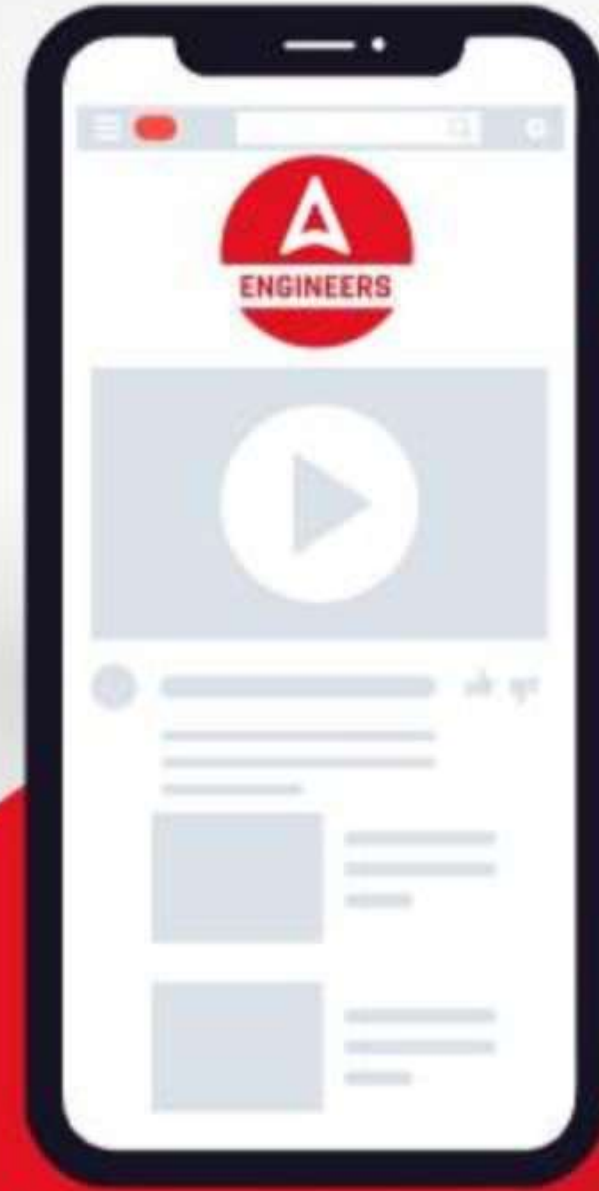


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Voltage

Transcond. $G = \frac{I_o \rightarrow \text{Output}}{V_i \rightarrow \text{Voltage}}$

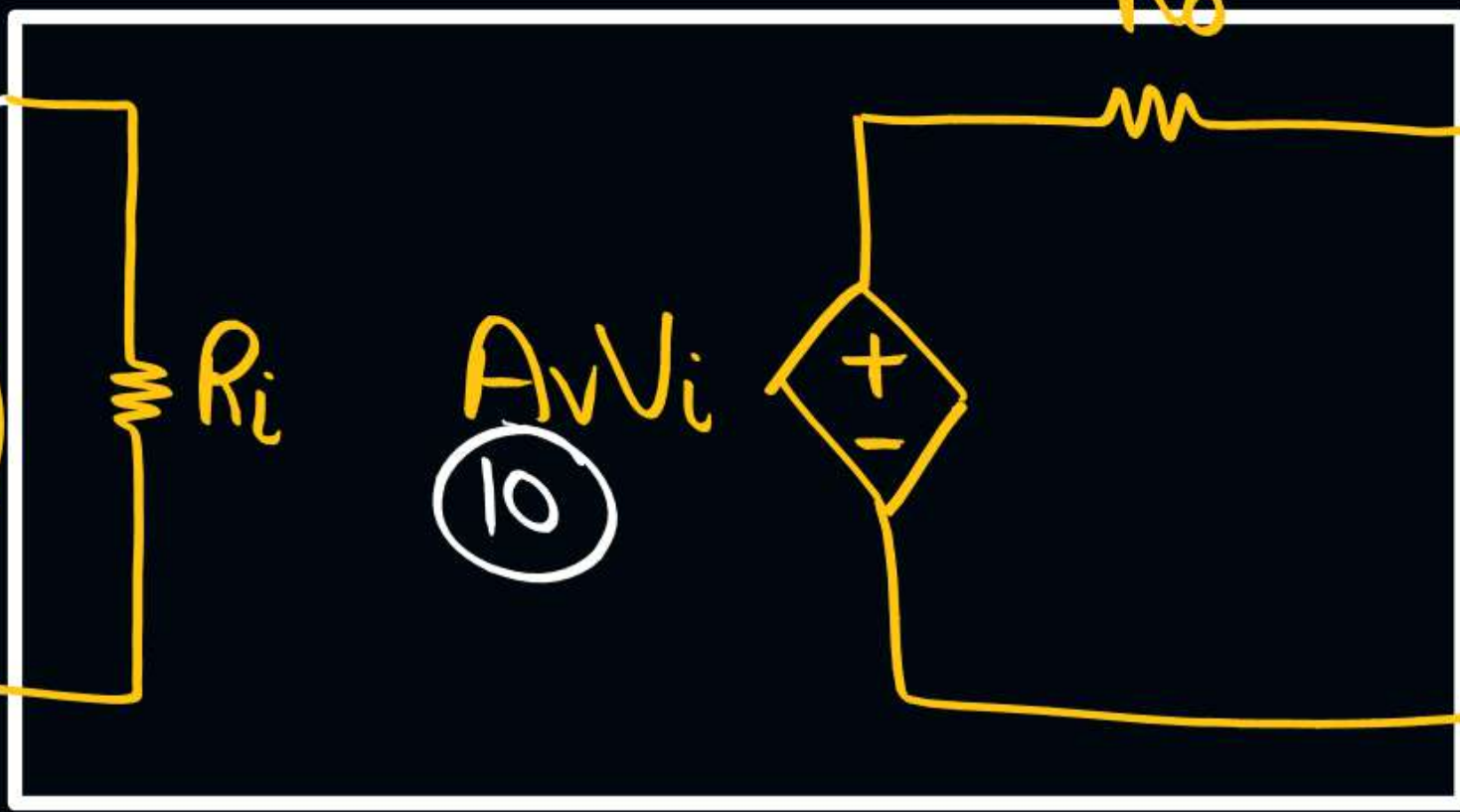
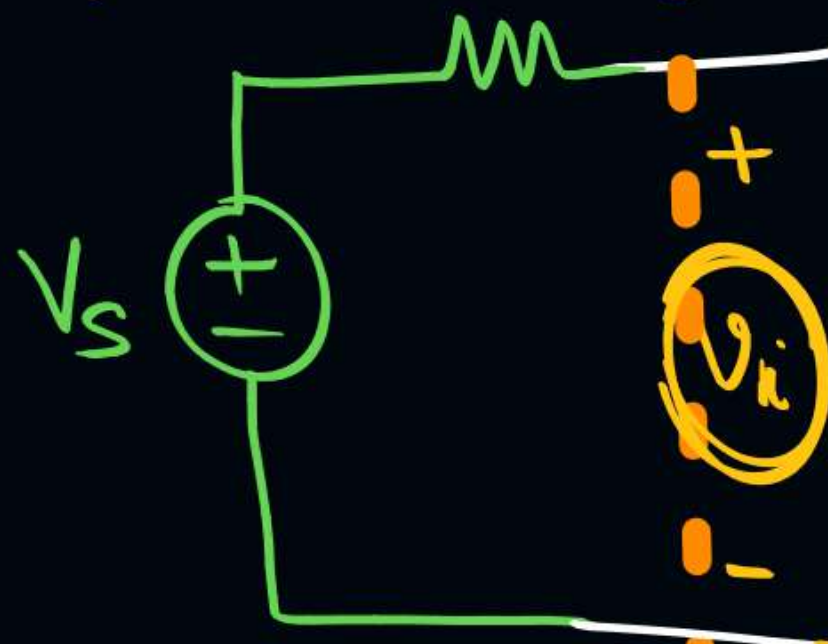
TransResistance $R_m = \frac{V_o \rightarrow \text{Output}}{I_i \rightarrow \text{Current}}$

Current : $A_f = \frac{I_o \rightarrow \text{Output}}{I_i \rightarrow \text{Input}}$

Equivalent Voltage Amplifier:

Customer

R_s



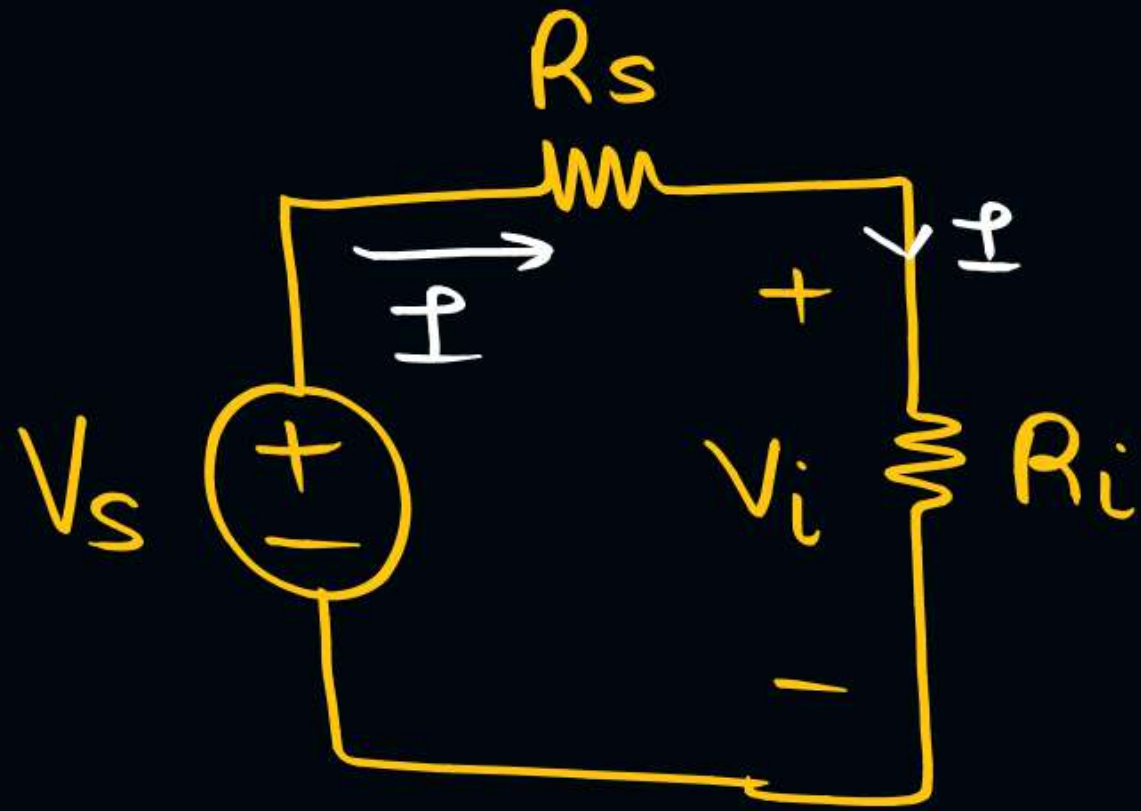
Amplifier

Customer

Load



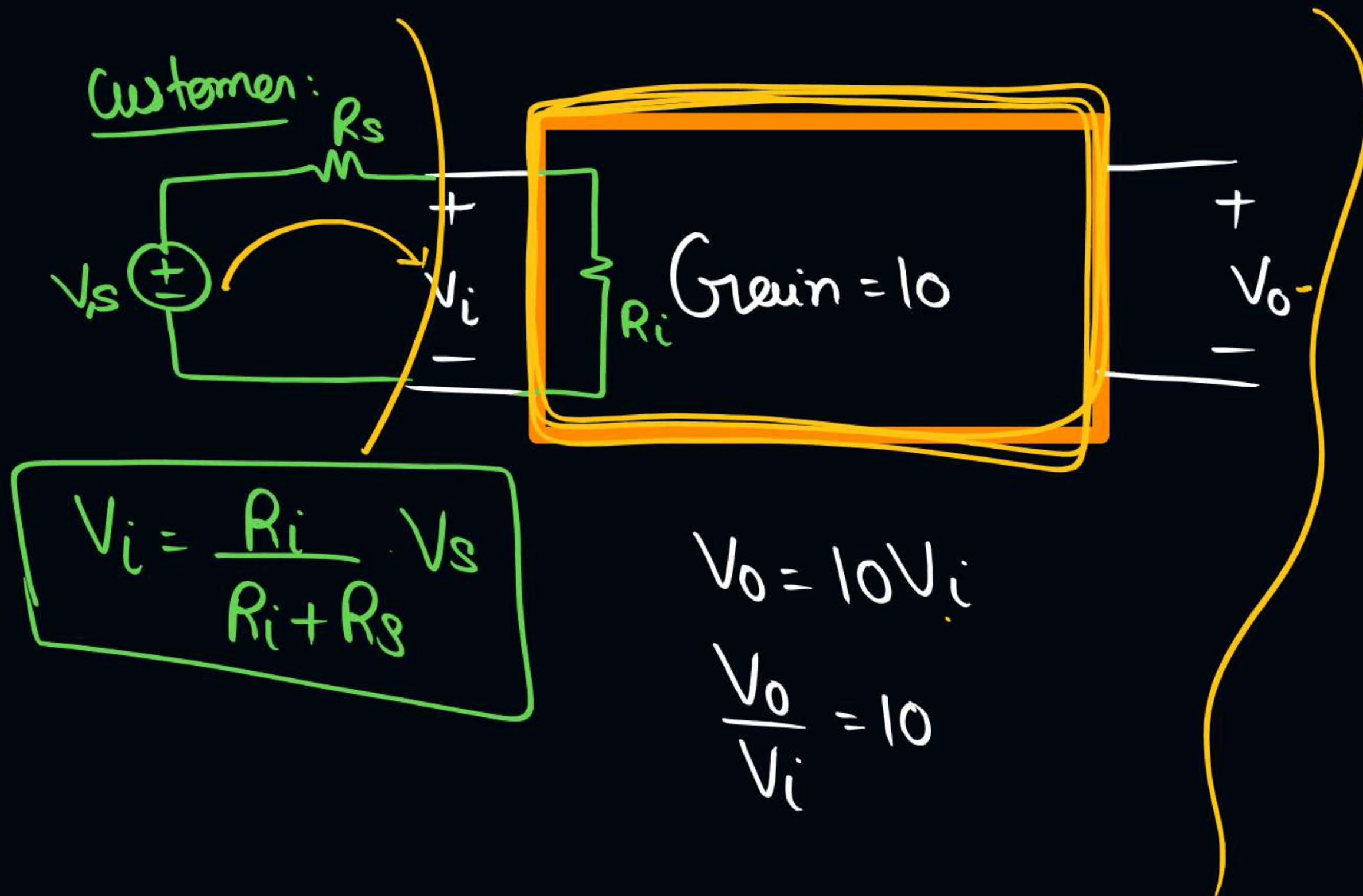
$R_i \rightarrow$ Input Resistance of Voltage Amplifier



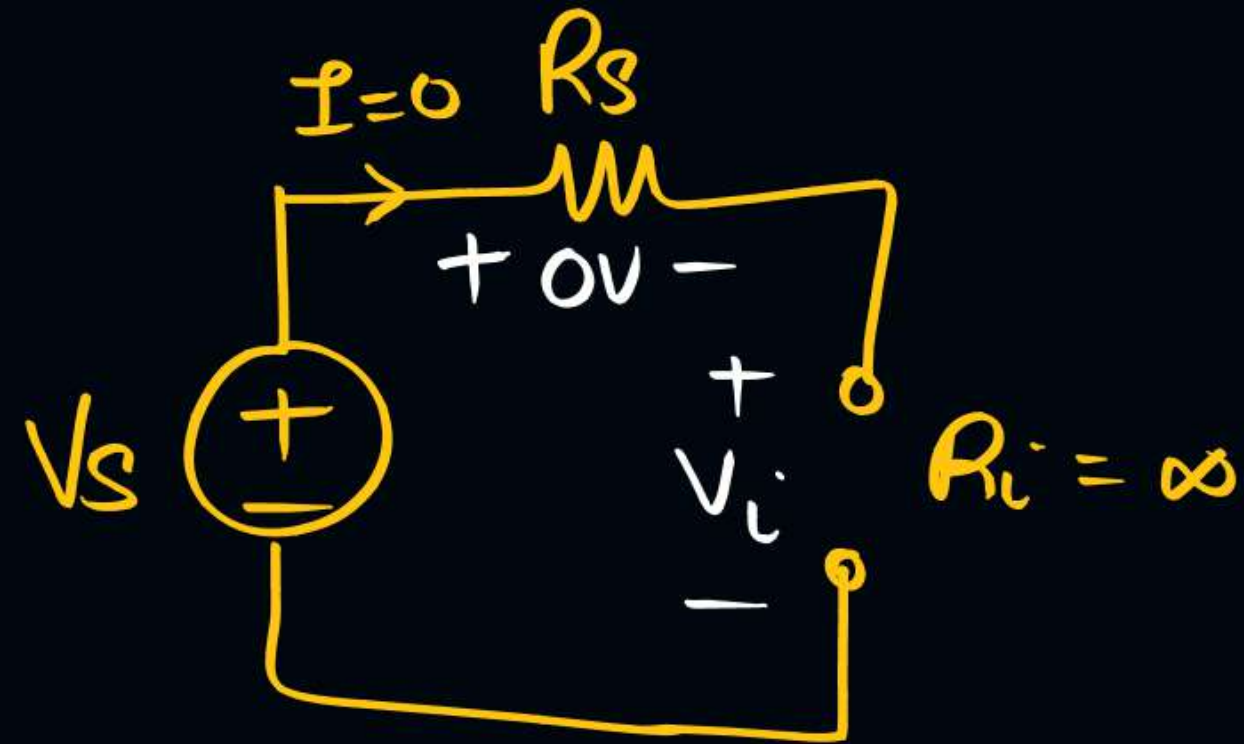
$$I = \frac{V_s}{R_s + R_i}$$

$$V_i = I \cdot R_i$$

$$V_i = \frac{V_s \cdot R_i}{R_i + R_s}$$



$$V_i = \frac{R_i}{R_i + R_s} \cdot V_s$$



Condition: $V_i = V_s$:

① $R_i = \infty$ (Ideal)

$R_h =$ Very very high (Practically)
 $= 1M\Omega$

$$V_s = 0 + V_i$$

$$V_s = V_i$$

Voltage Amplifier:

$$R_i = \infty \text{ (Ideally)}$$

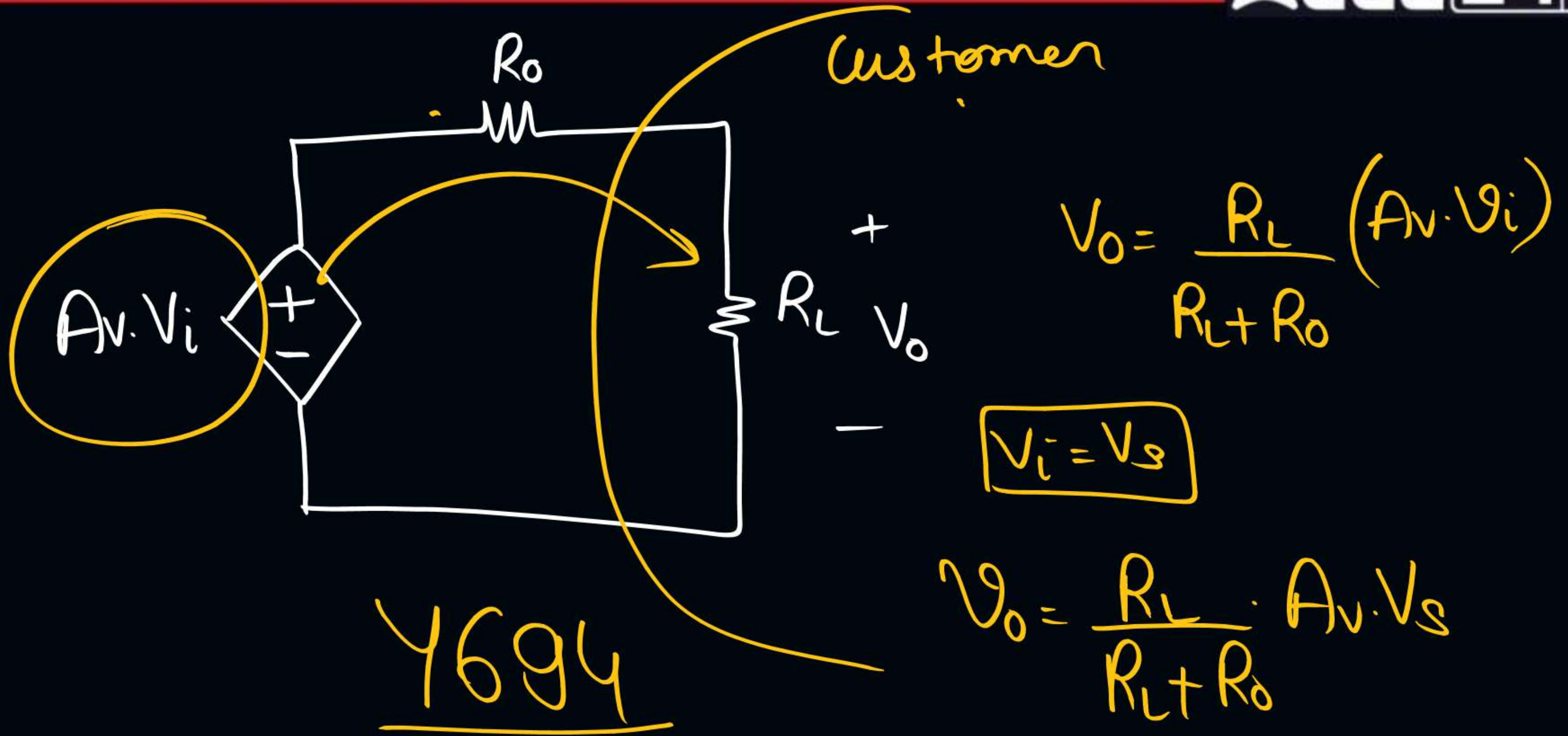
= very very high

$$= 1 \text{ M}\Omega$$

(Practically)

$$\underline{R_i = \infty:}$$

$$V_i = V_S$$



$$R_o = 0 \text{ (Ideally)}$$

$$R_o = \text{very very low}$$

$$V_o = \frac{R_L}{R_L + R_o} \cdot A_v \cdot V_s$$

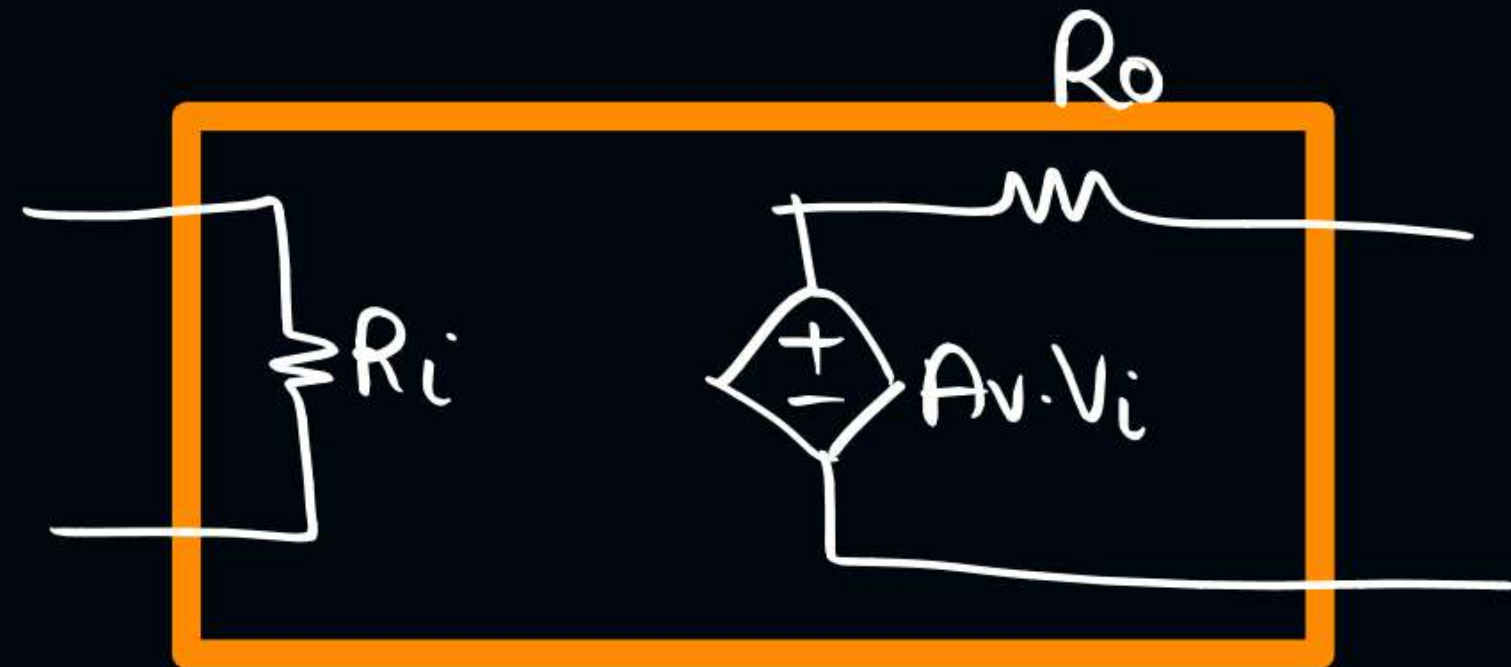
$$V_o = A_v \cdot V_s$$

$$A_v = \frac{V_o}{V_s}$$

$$V_o = 10 V_s$$

$$\text{if } A_v = 10$$

Voltage Amplifier:



$$A_v = \frac{V_o}{V_i}$$

$$R_i = \infty \text{ (Ideally)}$$

$$R_i = v.v. \text{ high (Practically)}$$

$$R_o = 0 \text{ (Ideally)}$$

$$R_o = v.v. \text{ low (Practically)}$$

Operational Amplifier is Voltage

Amplifier:

a) Input Resistance of OP-Amp is ∞

b) Output Resistance of OP-Amp is 0

Operational Amplifier:

<u>Char.</u>	Ideal	Practical
R_i	∞	$10^6 \Omega$
R_o	0	10-100 Ω
$A_v(\text{gain})$	∞	10^6

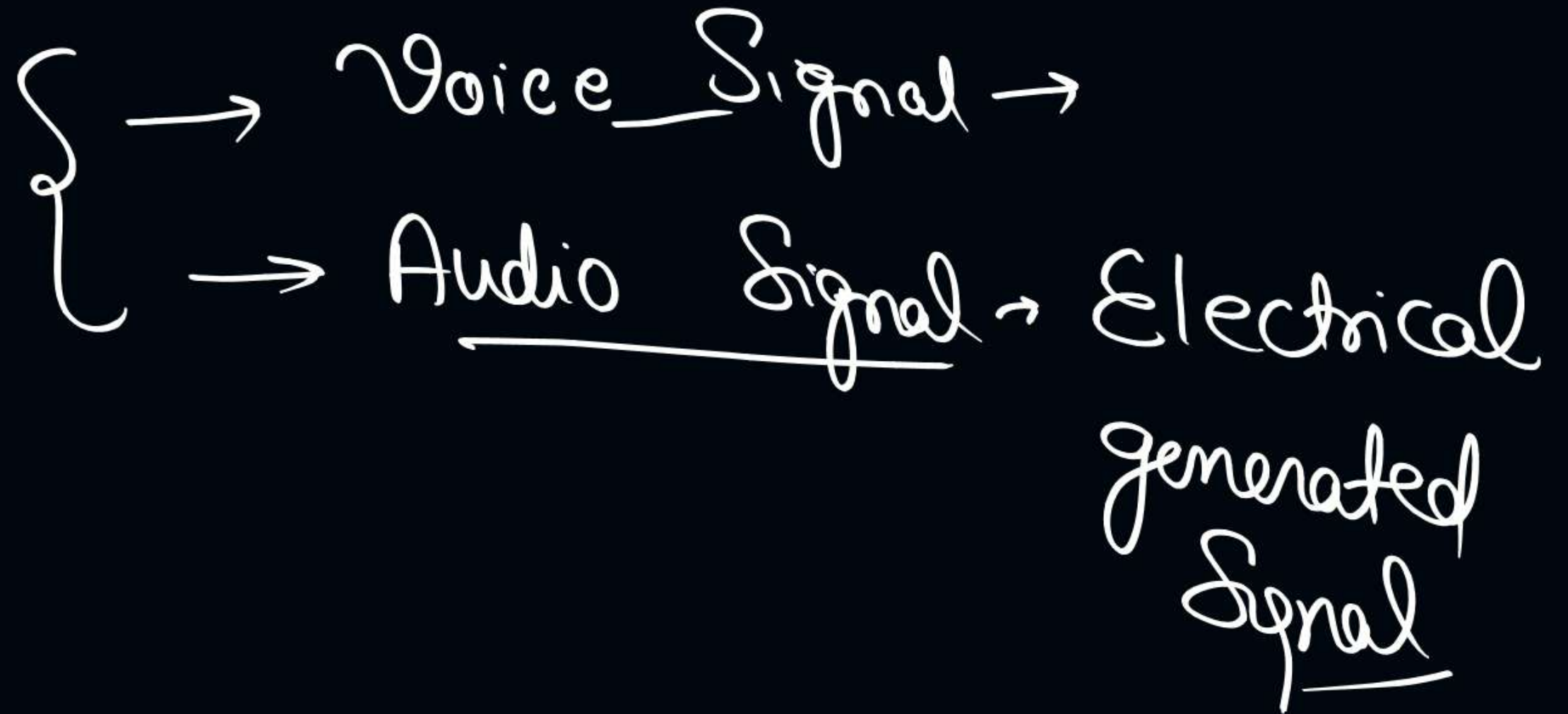
Char

Ideal

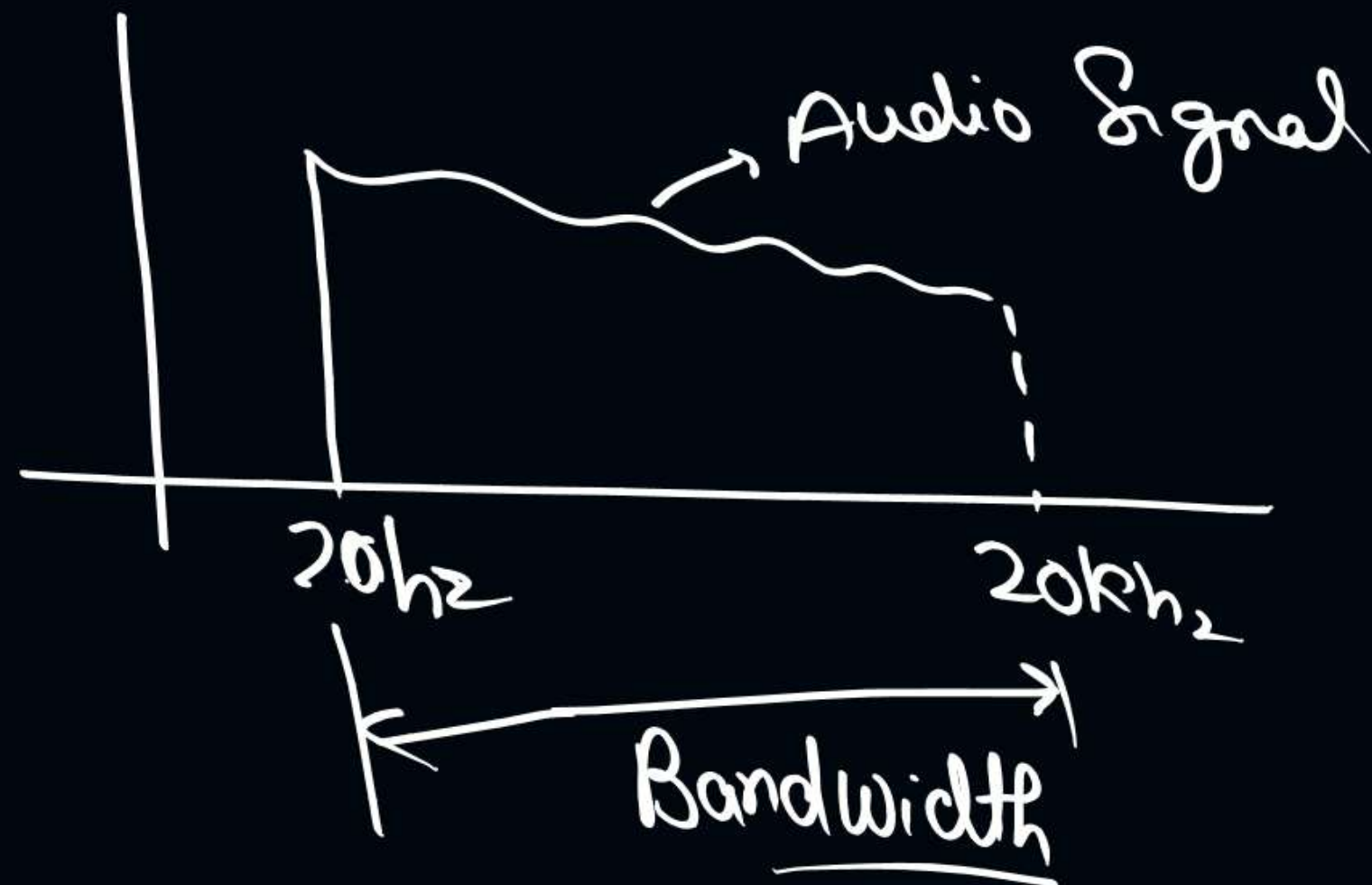
Practical

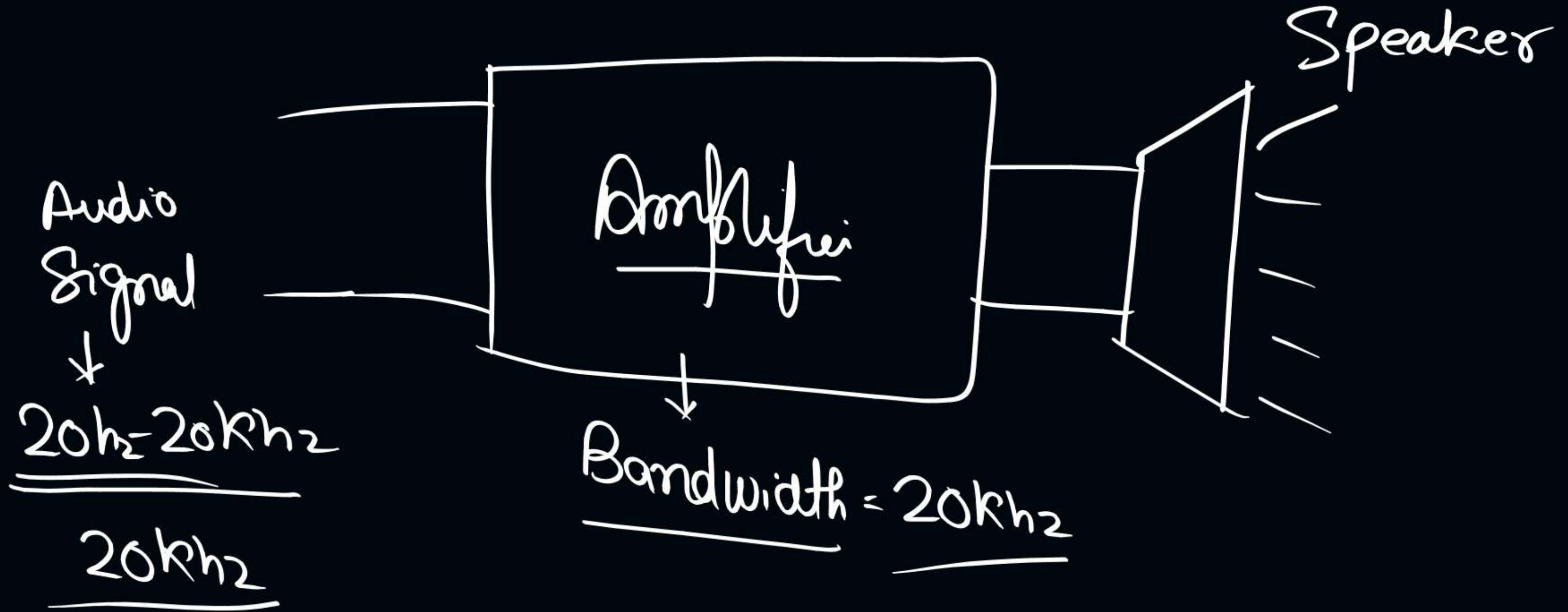
Bandwidth

Bandwidth:



Audio Signal: 20hz \rightarrow 20Khz





Char:

Basics of OP-Amp

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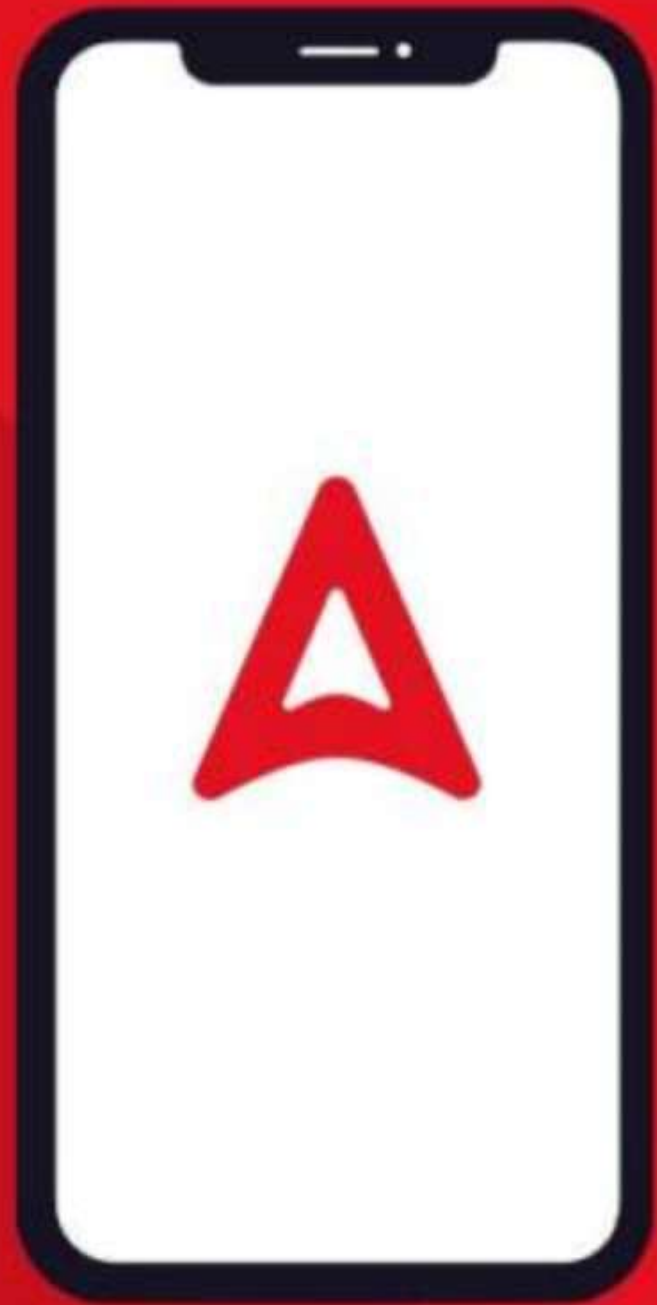
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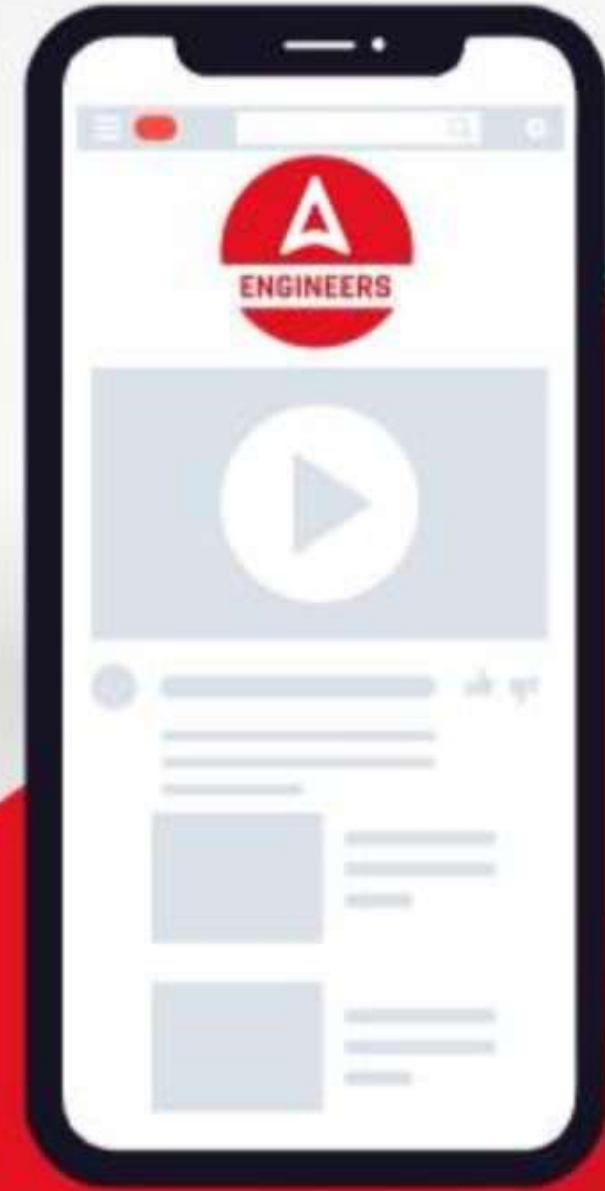
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