



RRB JE | SSE 2023

Foundation Batch

Analog Electronics

Day-10

> LIVE

2PM

LAWRENCE Sir



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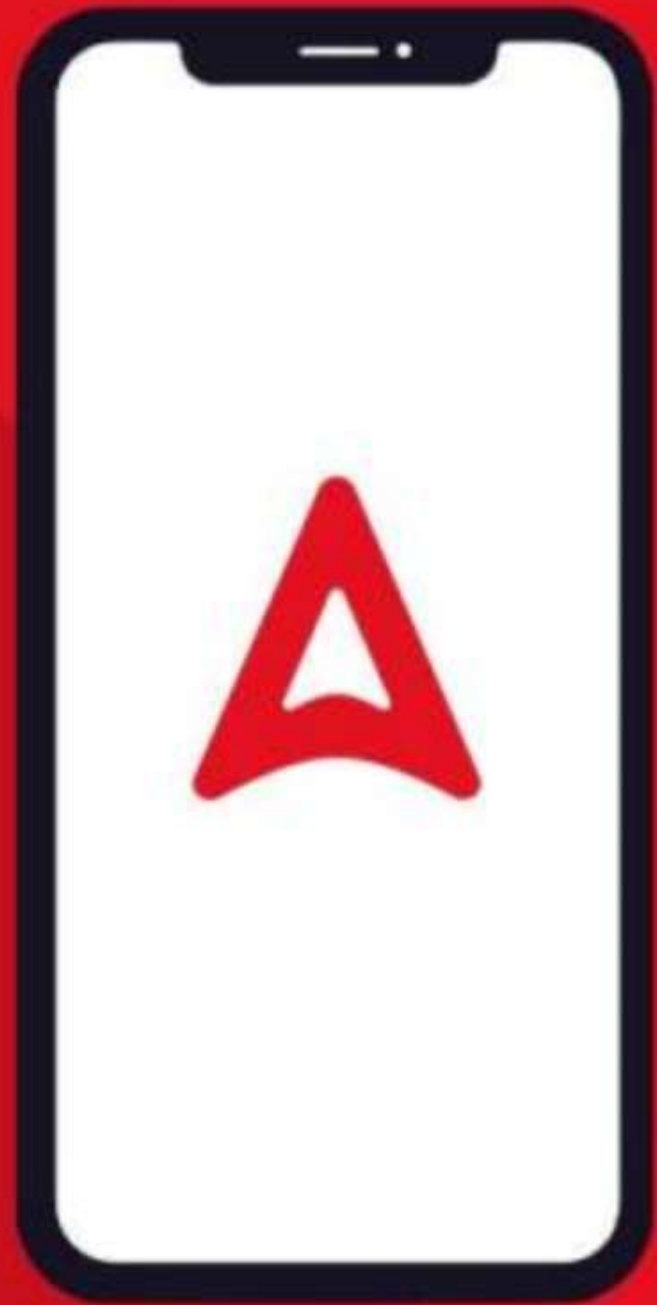
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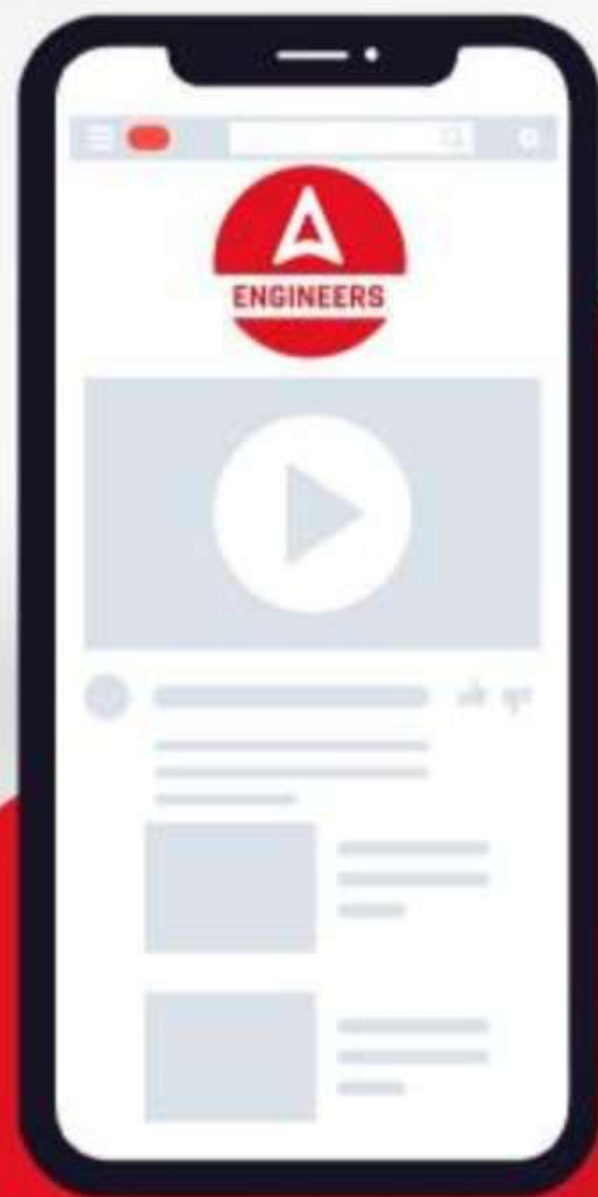
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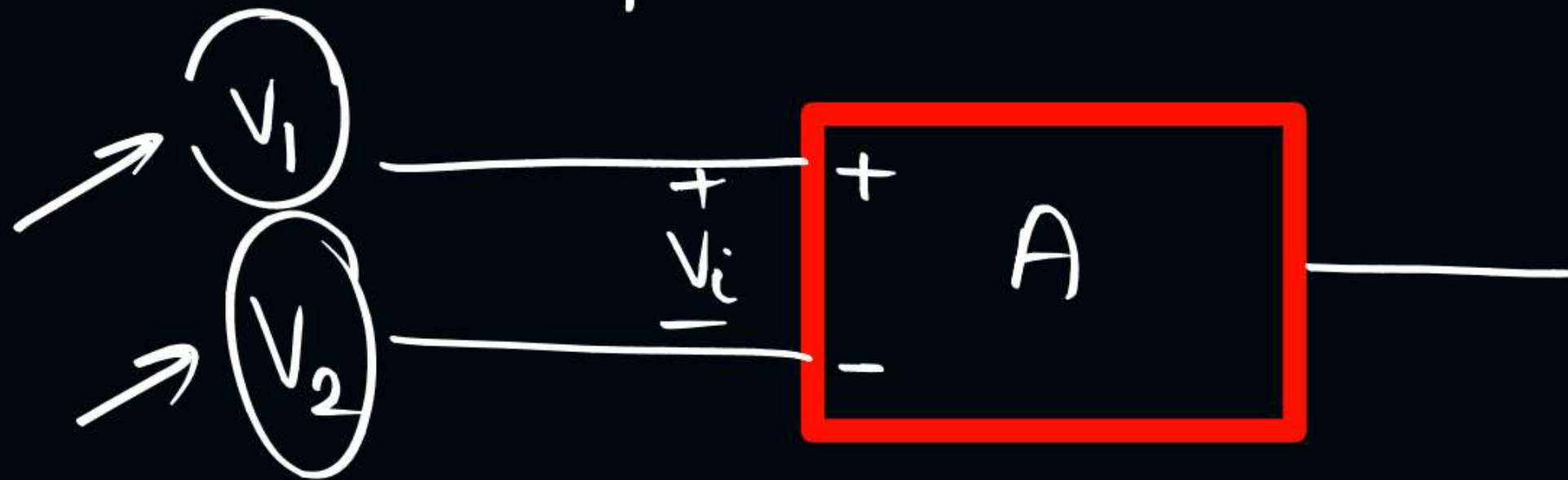


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→ Differential Mode:

Input → both terminals



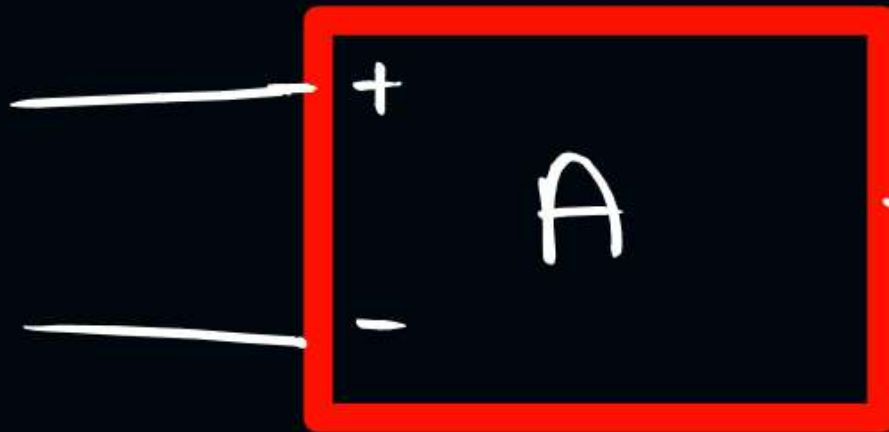
$$V_i = V_1 - V_2$$

$$V_o = A \cdot V_i$$

$$V_o = A(V_1 - V_2)$$

noise \rightarrow 1 μ volt

~~~~~  
~~~~~



$$V_o = 10^6 \times 1 \times 10^{-6} \\ = 1 \text{ volt}$$

$A_v \rightarrow 10^6$

»

$$V_o = A_d \cdot V_d + A_c \cdot V_c$$

Differential
Output Voltage

A_d → differential gain
 V_d → differential voltage

$$V_d = (V_1 - V_2)$$

$$V_o = A_d \cdot V_d + \underbrace{A_c \cdot V_c}$$

Common mode
output voltage

due to
noise

$A_c \rightarrow$ Common mode gain

gain provided
to noise

$V_c \rightarrow$ Common mode voltage

$$V_o = A_d \cdot V_d + A_c \cdot V_c$$

Q: What should be the value of common mode gain for ideal Op-Amp?

Ideally:

$$A_c = 0$$

Practically:

$A_c \rightarrow \text{v.v. low}$



Should be $V_0 = A(V_1 - V_2) = 0$

$\therefore \textcircled{1} V_1 = V_2 = V_S$

if $V_0 = 1 \text{ volt}$ \rightarrow due to common mode
 \downarrow
 $A_c \cdot V_c$

$$V_o = A_d \cdot V_d + A_c \cdot V_c$$

A_d = differential gain

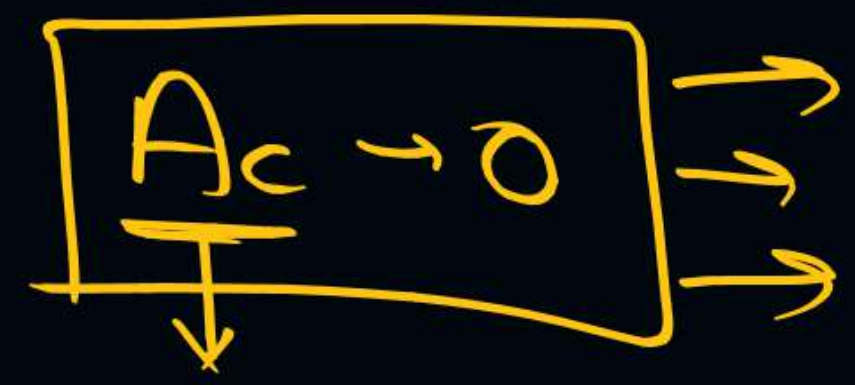
V_d → differential voltage

$$V_d = V_1 - V_2$$

A_c → common mode gain

V_c → common mode voltage

$$V_c = \frac{V_1 + V_2}{2}$$



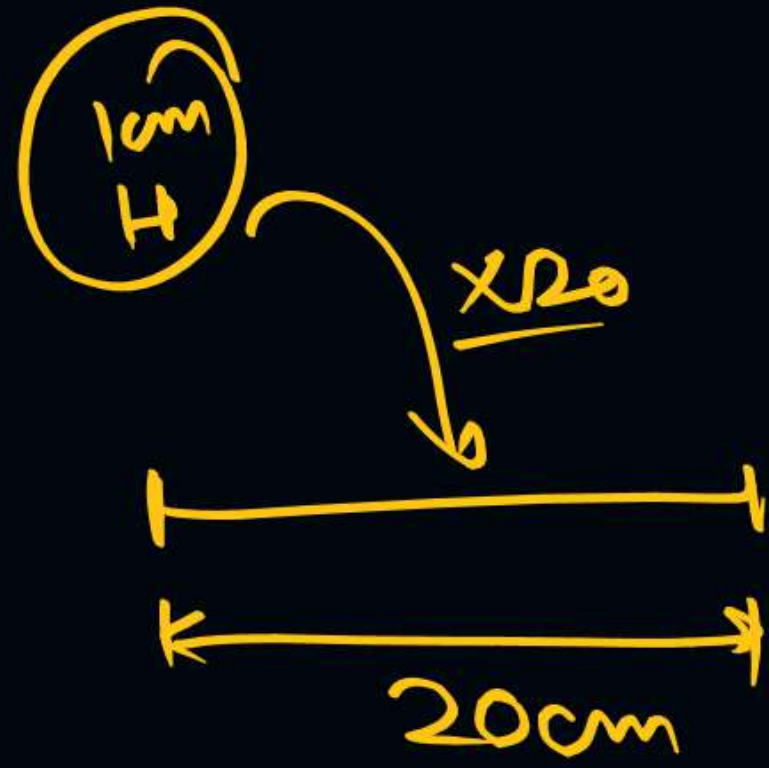
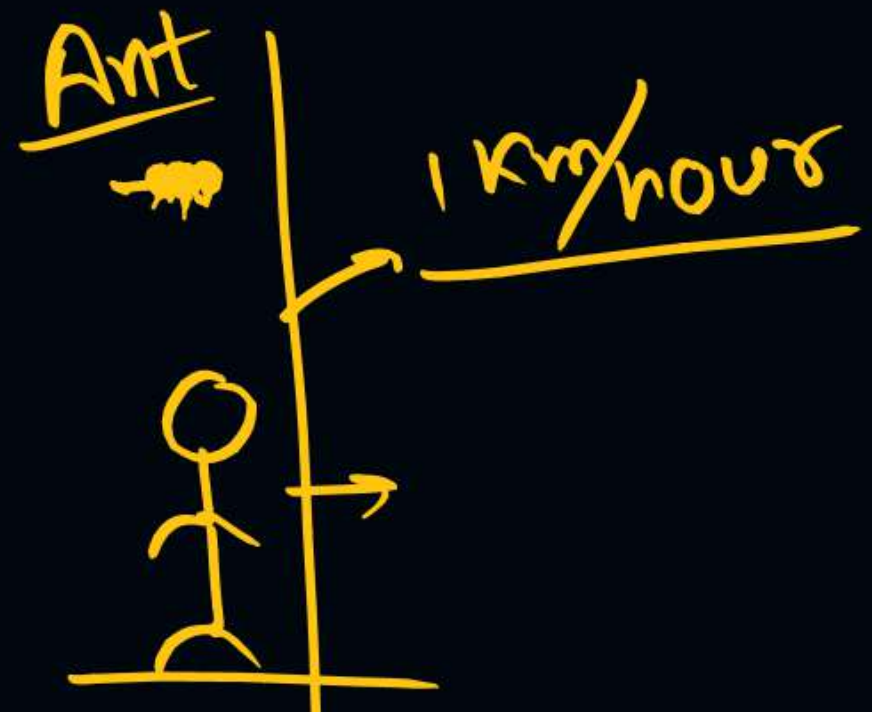
Common Mode Rejection Ratio
(CMRR)

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CMRR =

↗



- a) Slow
- b) fast
- c) normal

Amplifier:

①

②

Differential gain



high



①
better

Common Mode gain



low

$$\text{Common Mode Rejection Ratio} = \frac{A_d}{A_c} = \text{CMRR}$$

↓
It is Ratio of differential gain to
Common Mode gain

Q: What should be the value of CMRR for ideal Op-amp?

- a) 0
- b) ∞
- c) v. high
- d) depends on char.

$$CMRR = \frac{A_d}{A_c}$$

Ideal: $A_c = 0$ $CMRR = \infty$

Practical: $A_c = v. \text{ low}$, $CMRR = v. \text{ high}$

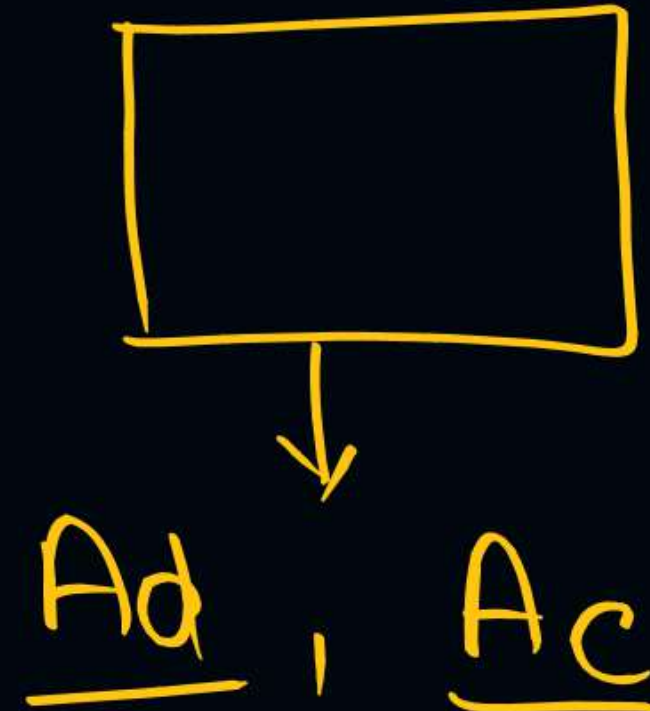
7.

$$V_o = A_d \cdot V_d + A_c \cdot V_c$$

For A_d :

$$V_d = V_1 - V_2$$
$$V_c = \frac{V_1 + V_2}{2}$$

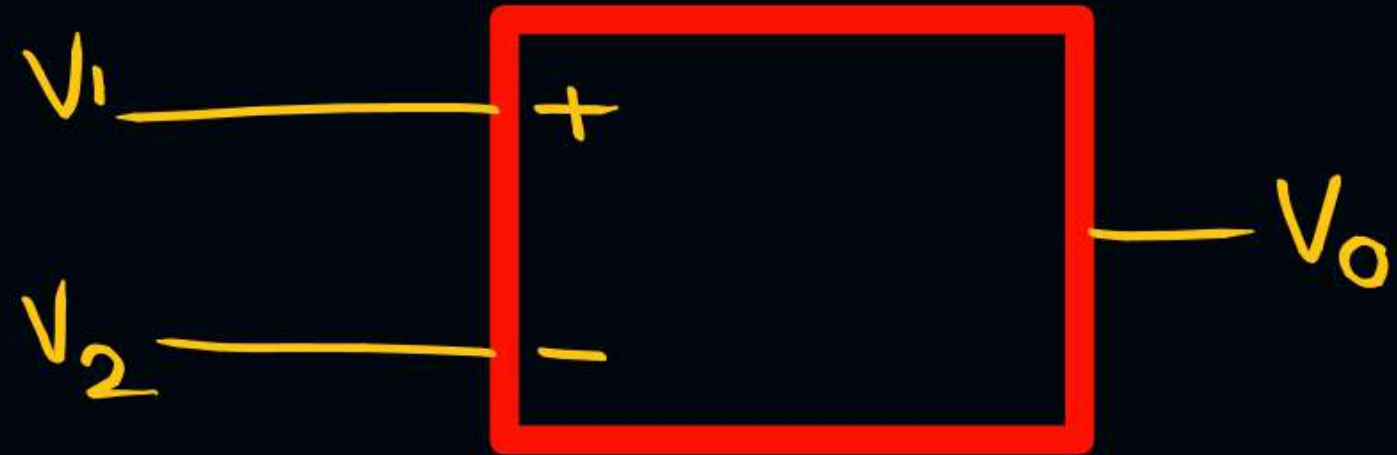
For A_c :



for Ac:

$$\textcircled{1} \quad V_1 = V_s$$
$$V_2 = V_s$$

$$V_d = (V_1 - V_2)$$
$$V_d = 0$$



$$V_c = \frac{V_s + V_s}{2} = V_s$$

$$V_o = A_d \cdot V_d + A_c \cdot V_c$$

$$V_o = A_d \cdot 0 + A_c \cdot V_s$$



$$A_c = \frac{V_o}{V_s}$$

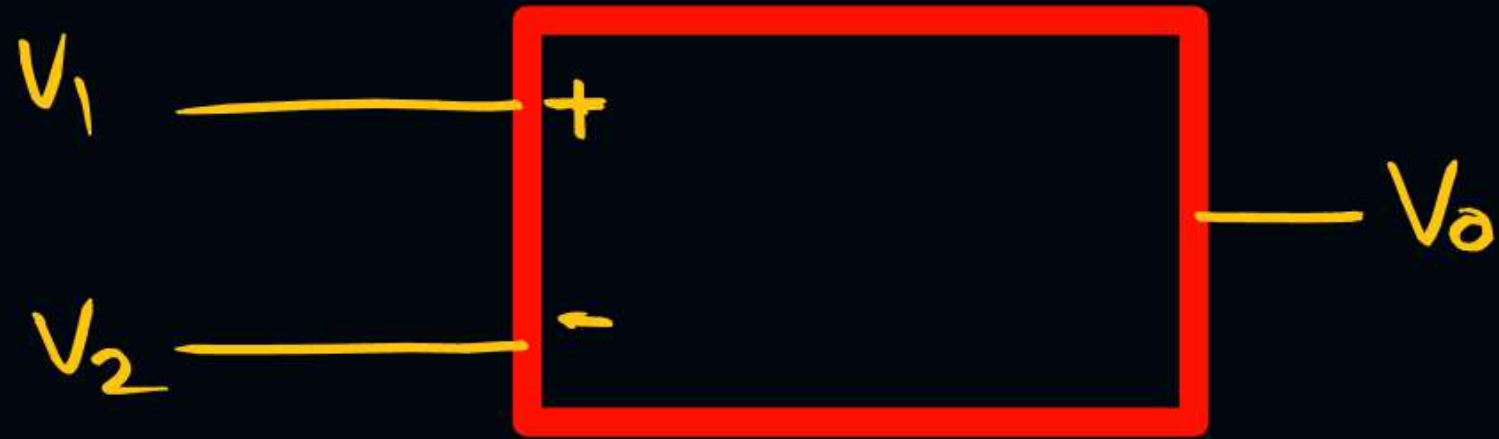
→ Common Mode Gain

$$A_c = \frac{V_o}{V_s}$$

For Ad:

$$V_1 = \frac{V_s}{2}$$

$$V_2 = -\frac{V_s}{2}$$

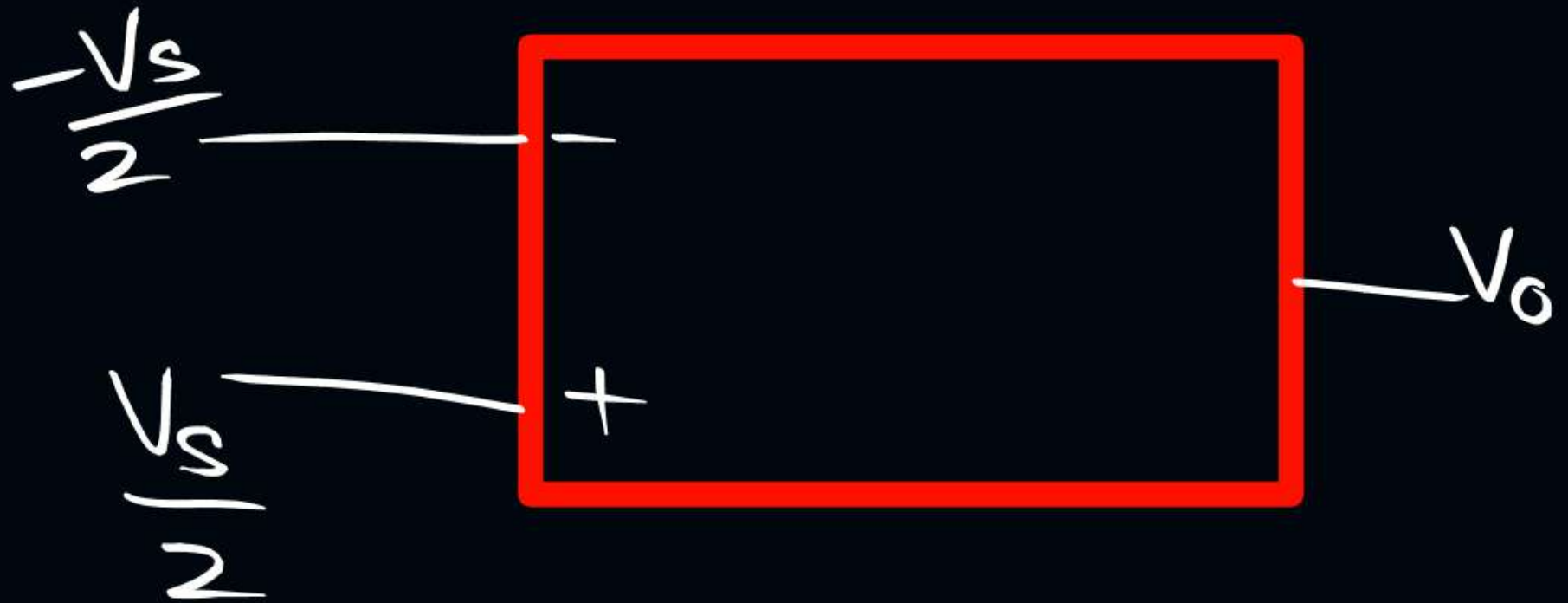


$$V_d = \frac{V_s}{2} - \left(-\frac{V_s}{2}\right) = V_s$$

$$V_c = \frac{\frac{V_s}{2} - \frac{V_s}{2}}{2} = 0$$

$$V_o = A_d \cdot V_s + 0$$

$$A_d = \frac{V_o}{V_s}$$



$$\textcircled{gf} \rightarrow V_1 = V_s, \quad V_2 = -V_s$$

$$V_d = V_s - (-V_s) = 2V_s$$

$$V_c = \frac{V_s - V_s}{2} = 0$$

$$V_o = A_d \cdot (2V_s) + A_c (0)$$

★

$$A_d = \frac{V_o}{2V_s}$$

$$CMRR = \frac{A_d}{A_c}$$

★
★

Ideally $A_c = 0$
 $CMRR = \infty$

$$A_c = v \cdot v \cdot \text{low}$$

$$CMRR = v \cdot v \cdot \text{high}$$

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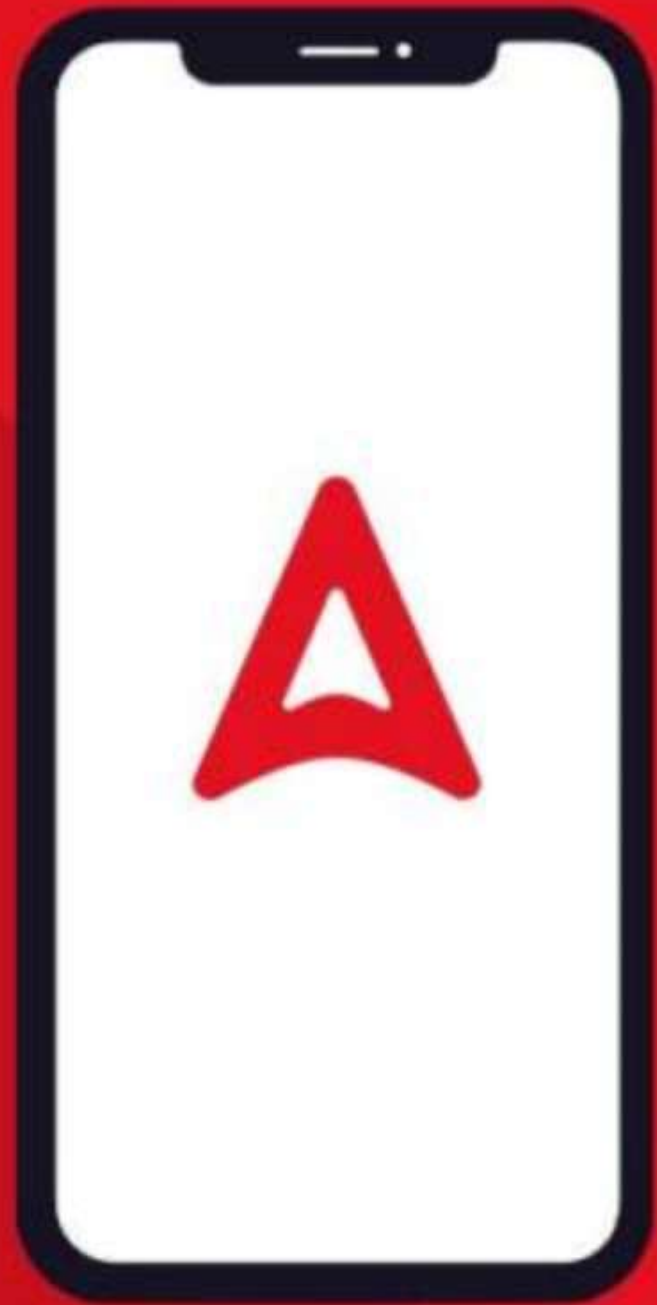
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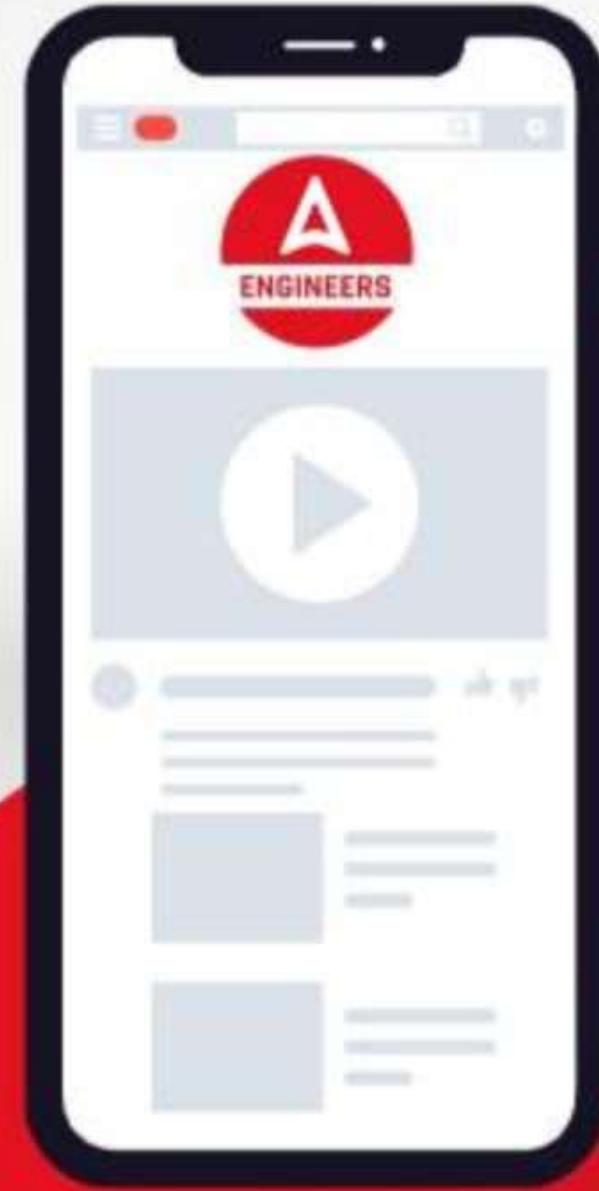
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