

WELCOME
TO Adda247

***“STEADY STATE
IS JUST AN
ILLUSION.”***



CICUIT THEORY
QUESTIONS SESSION
(AMOST SIMILAR TO GATE EXAM)

EVERY SATURDAY 11 AM

ON ADDA 247 APP

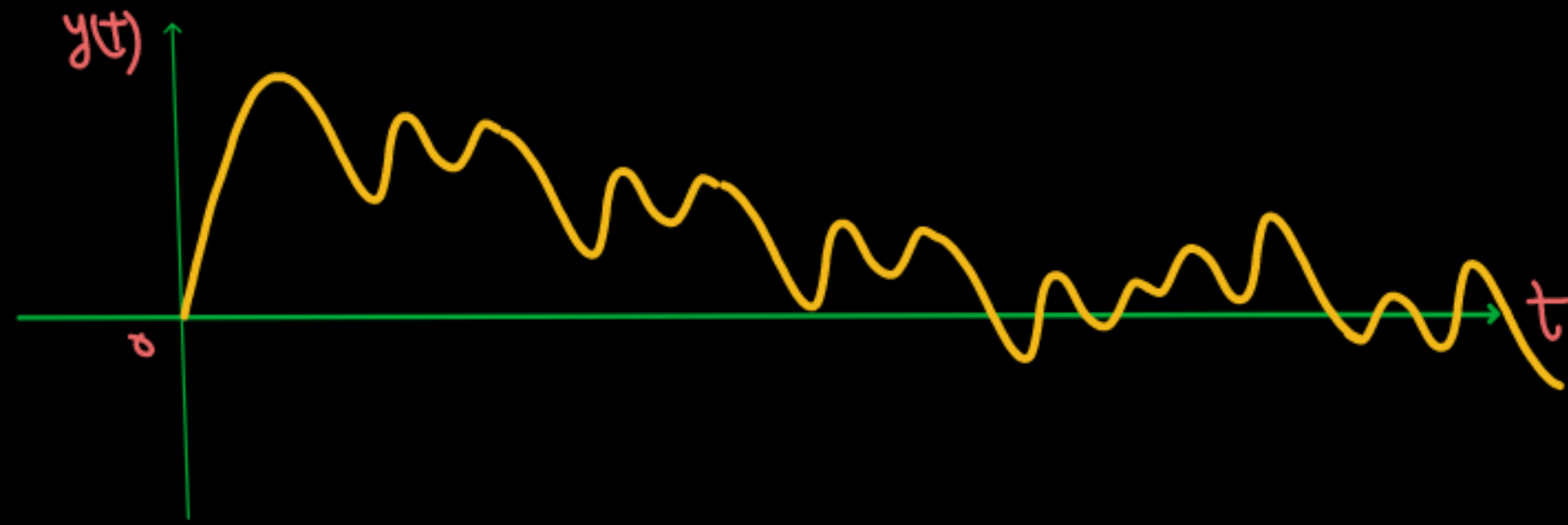
PLZ INSTALL APP

(+503)

LINK GIVEN IN COMMENT BOX

Chapter - 2

DC Transient and Steady State analysis

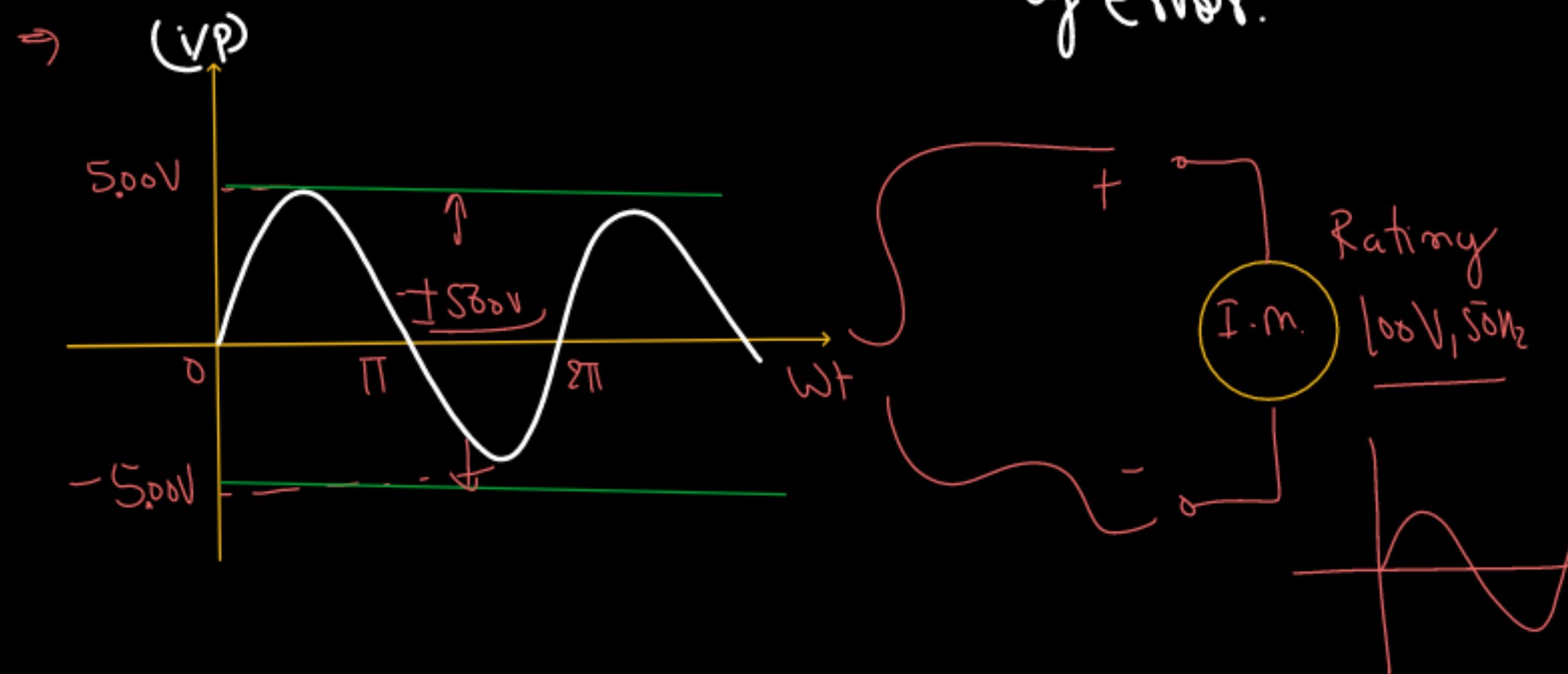


Transient \Rightarrow unwanted resp.

Steady state \Rightarrow Needed resp.

Absolute steady state:- "it impossible to achieve."

* S.S. always appears with finite amount of error.



USE CODE Y503 to Join ADDA247 Paid Classes and GET Max Discount

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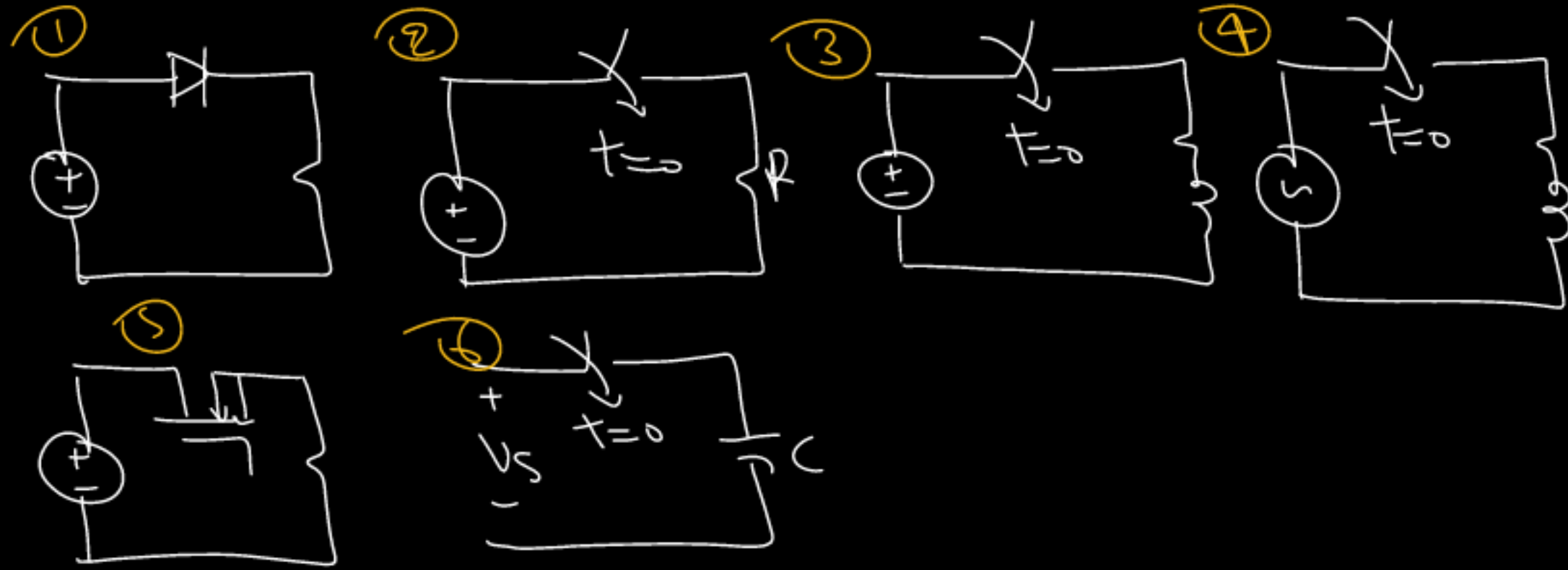
Response of a system.

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Understanding of Transient and Steady State.

Transient response.

Why Transient introduces in response.

* here All system, will introduce transient.

* it is impossible, to design any system free from transient

ex- transient in the response, mainly comes due to

- (a) i/p (b) system (c) fault (d) Sudden change.

⇒ to reduce transient, we have to improve our system.

Why Transient introduces in response.

transient introduces in the resp., Due to -

i) temp. variation.

ii) atmosph. condition.

iii) L/C

iv) Semiconductor devices

v) alloy -

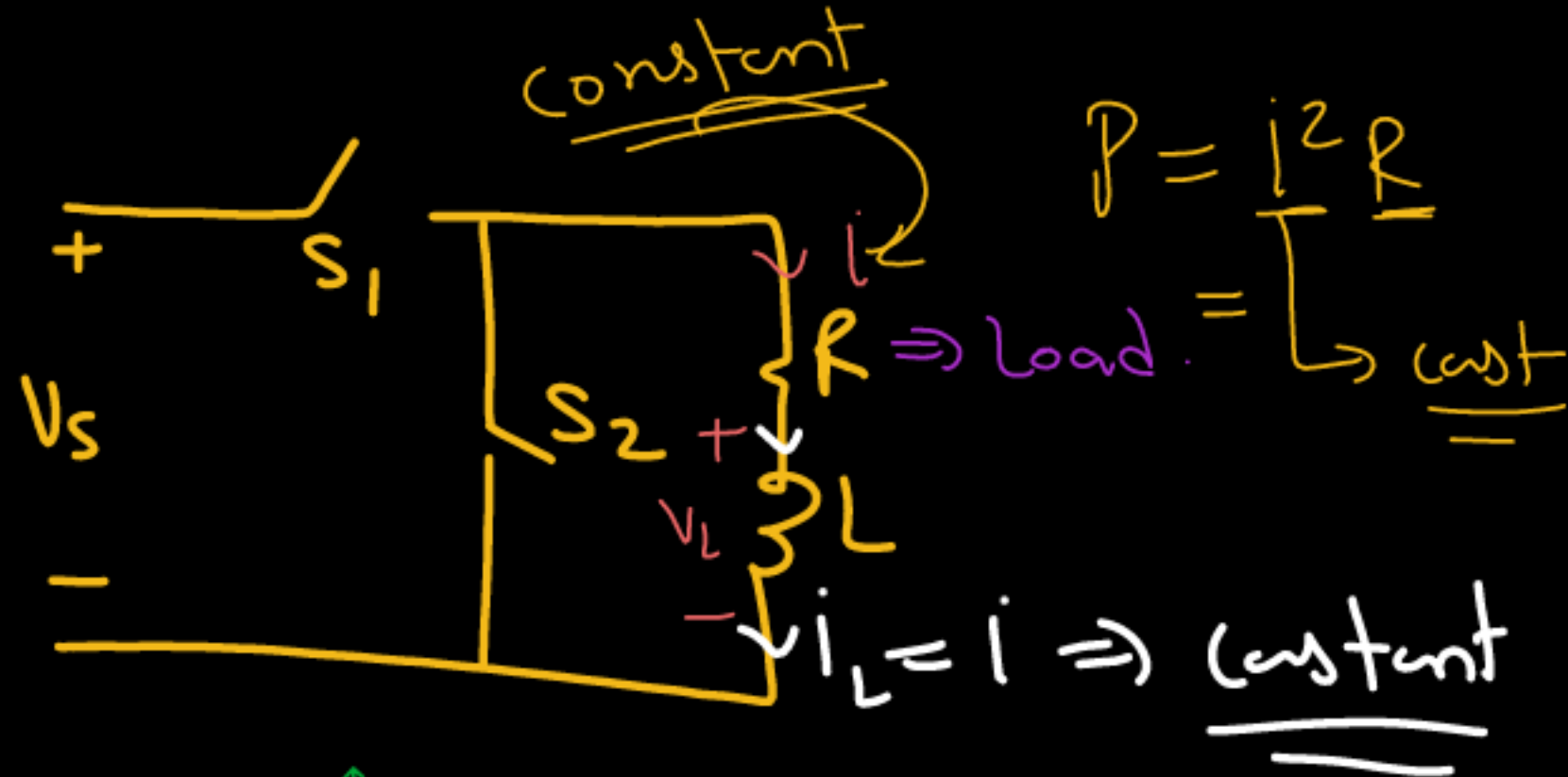
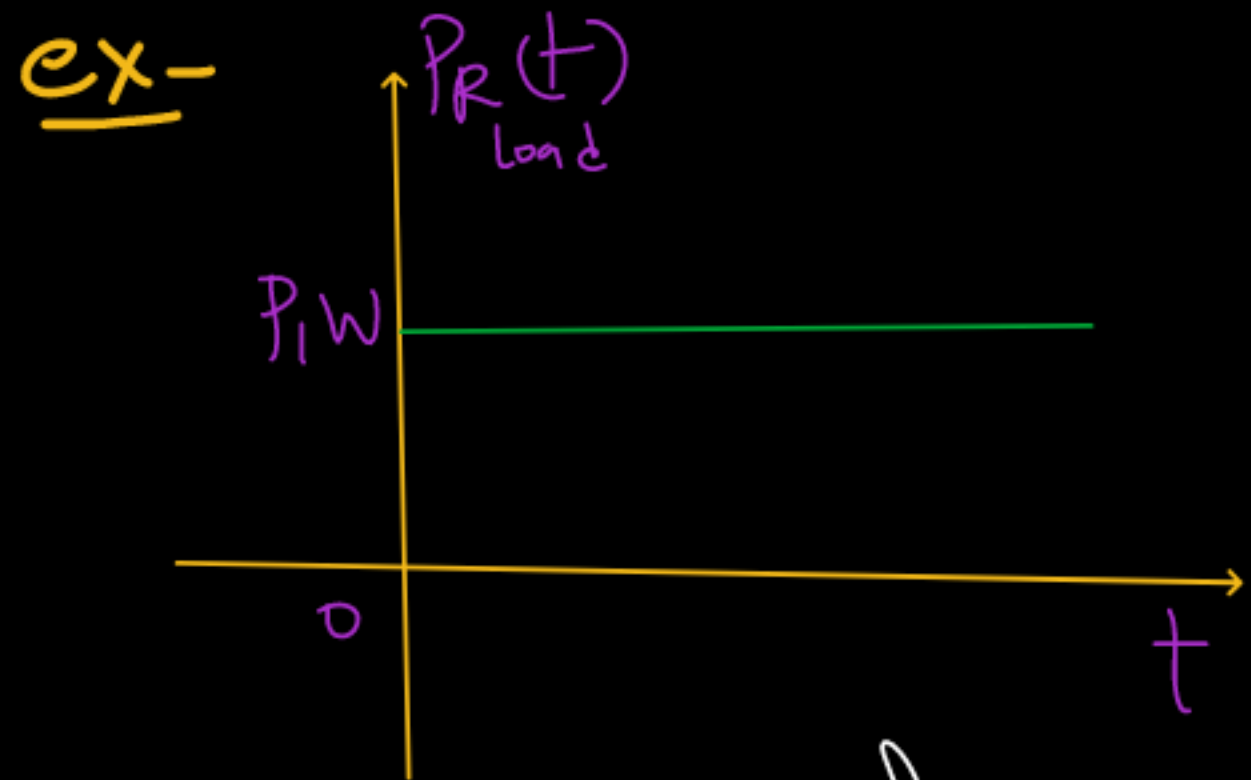
vi) Pressure change -

vii) etc. -

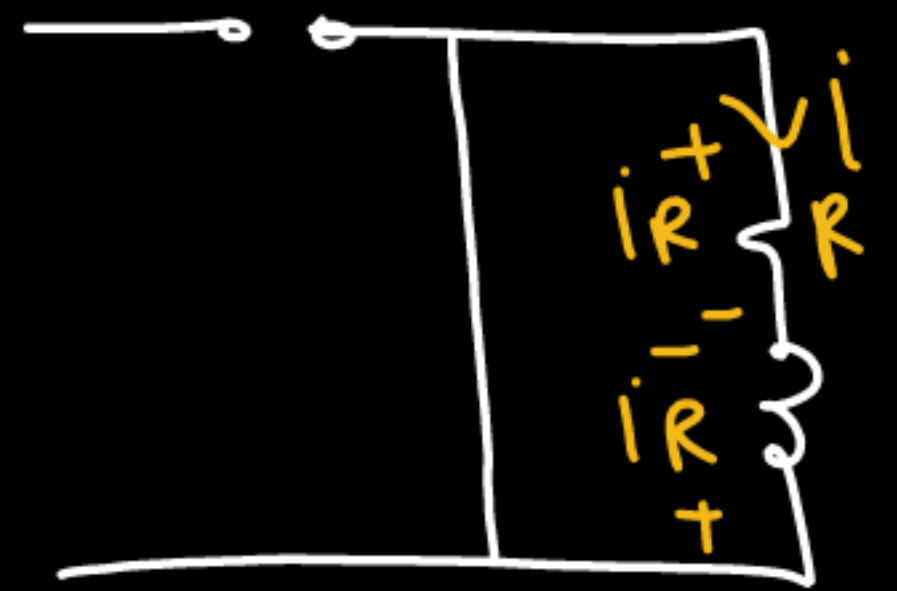
Passive Elements.

1. Inductor
2. Capacitor
3. Resistor

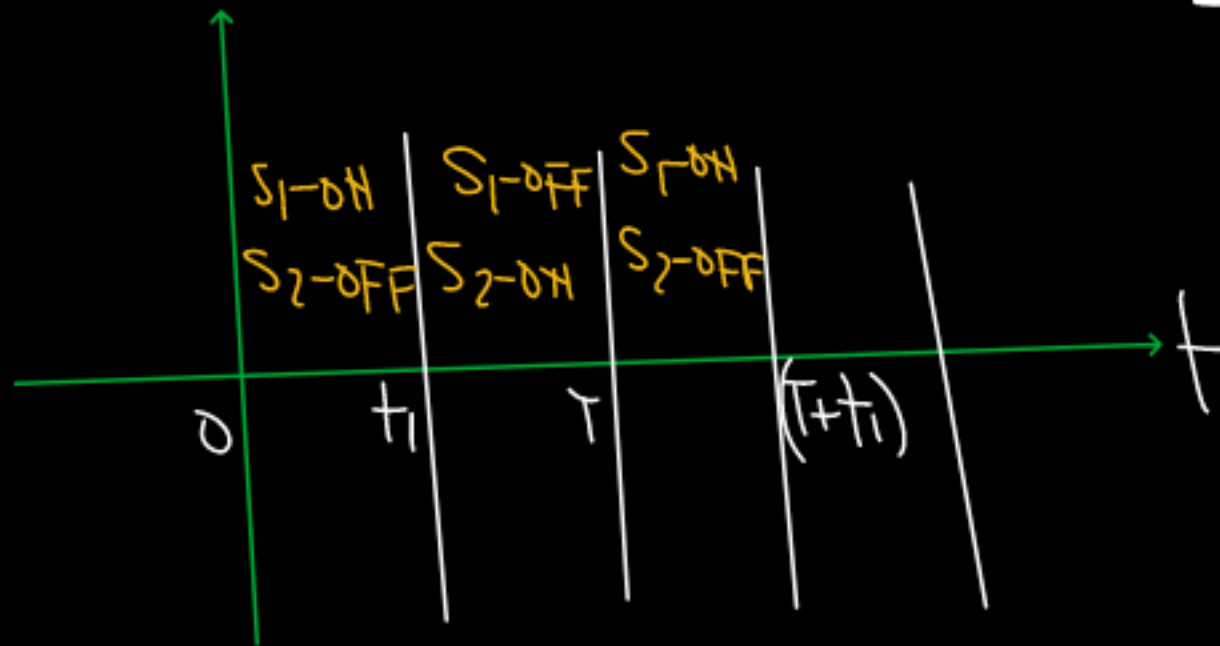
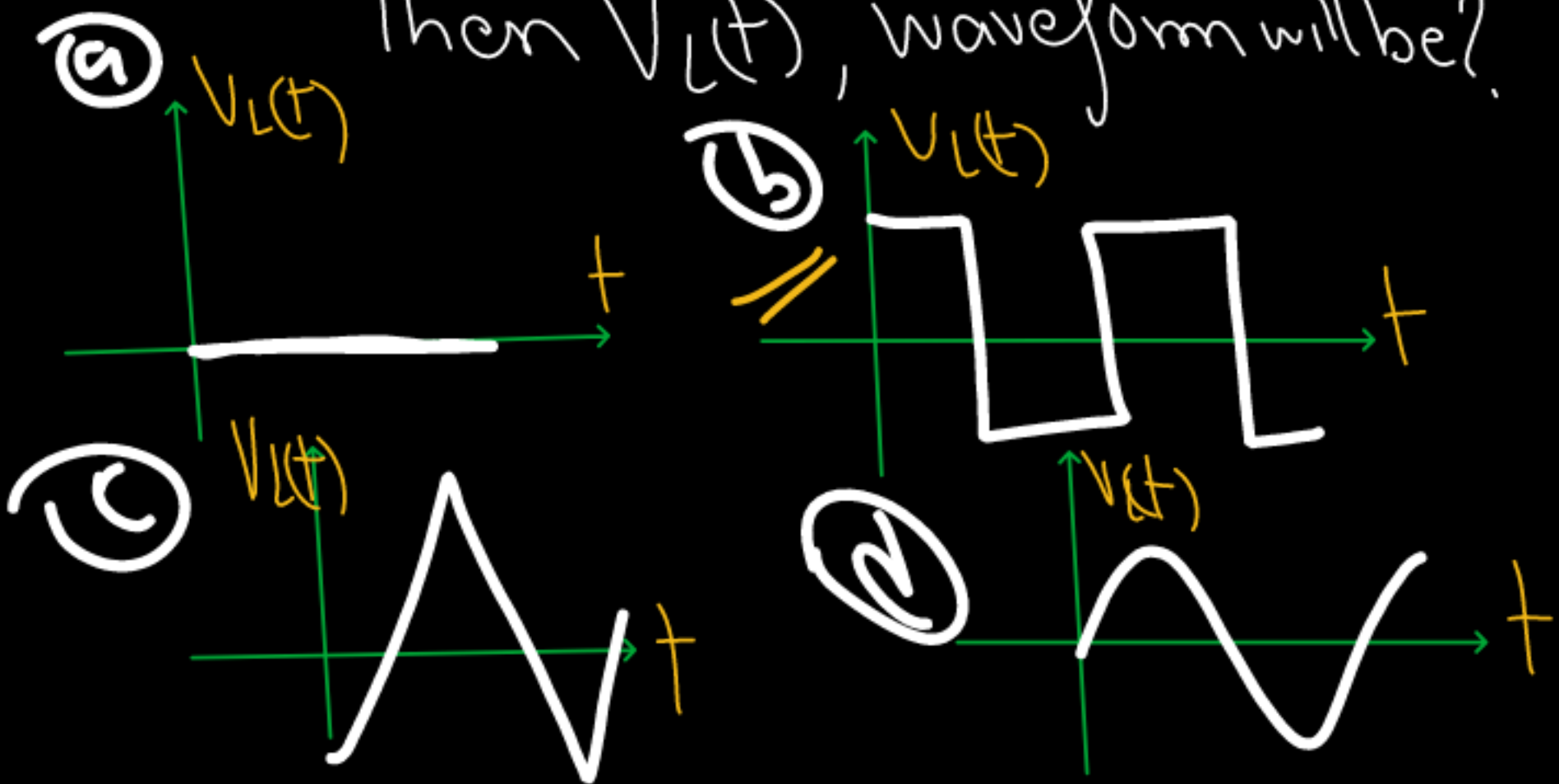
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Solunⁿ -

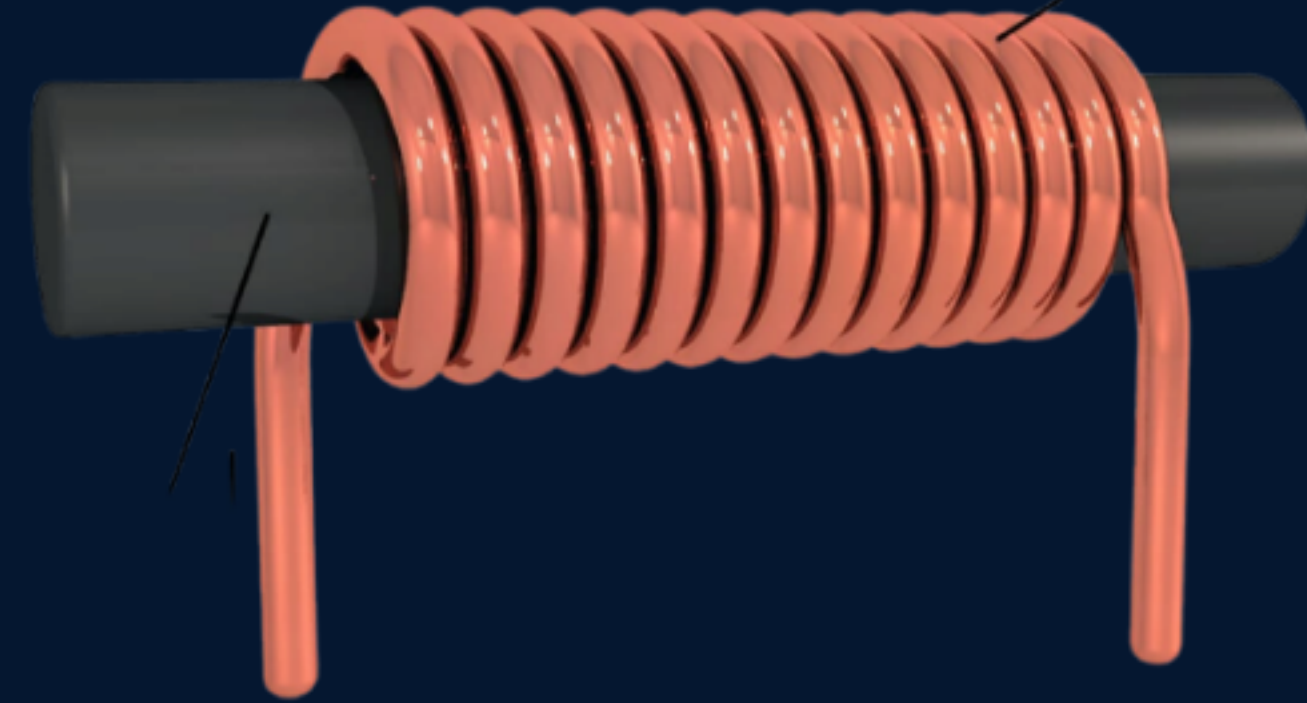


Then $V_L(t)$, waveform will be?



1. Inductor

INDUCTOR



INDUCTOR

