

# WELCOME

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*"If you can think, you can  
Achieve"  
So start thinking..*

*Renu Raj Garg  
M.Tech (VLSI Design)  
13 Year of Teaching  
Experience  
Worked 10 Year in NTRO*

# GATE 2024



**प्रवाह** Batch

## COMMUNICATION

**QUESTIONS FROM BASE BAND  
DATA COMMUNICATION**

**TIME- 9:00PM**

**RENU SIR**



Chapter-2

**Digital Communications**

*In today's lecture we will cover the following Topics :*

- 1. Questions from Base Band Data Communication*



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# GATE 2023 RESULT



**Congratulations**  
**FROM ADDA 247 FAMILY**

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<b>AIR</b> <b>64</b> <b>CE</b> UTKARSH MISHRA	<b>AIR</b> <b>71</b> <b>EE</b> SONESH SANJAY PAKAR	<b>AIR</b> <b>76</b> <b>CE</b> DIPANKAR DAS	<b>AIR</b> <b>87</b> <b>EC</b> SURAJIT RABI DAS	<b>AIR</b> <b>91</b> <b>EE</b> RISHABH GUPTA	<b>AIR</b> <b>111</b> <b>ES</b> ANIL GUPTA
<b>AIR</b> <b>130</b> <b>EE</b> SAURAV PATEL	<b>AIR</b> <b>136</b> <b>CE</b> RUPESH SACHDEVA	<b>AIR</b> <b>200</b> <b>ECE</b> WASIUZZAMA	<b>AIR</b> <b>212</b> <b>IN</b> WASIUZZAMA	<b>AIR</b> <b>217</b> <b>ME</b> VISHAL KUMAR	<b>AIR</b> <b>219</b> <b>ME</b> NITISH KUMAR
<b>AIR</b> <b>258</b> <b>EE</b> MANAV	<b>AIR</b> <b>348</b> <b>EE</b> AMAN NAMDEV	<b>AIR</b> <b>392</b> <b>EE</b> GAURAV MAHAJAN	<b>AIR</b> <b>403</b> <b>EC</b> MOHAN KUMAR SINGH	<b>AIR</b> <b>567</b> <b>EE</b> SHANKAR JHA	<b>AIR</b> <b>571</b> <b>ME</b> VUENDER MEENA



GATE

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**BILINGUAL**

# **PRACHAND BATCH**

## **FREE FOR ALL**

**ELECTRICAL,  
ELECTRONICS COMMUNICATION ENGINEERING**

### **GATE 2024 & ALL PSU's**



**Start Apr 11, 2023**      **7:30 AM to 11:30 PM**

Free

# You **Tube** Classes Schedule



## EE & EC ENGINEERING

EXAM TARGET	SUBJECT	TIME	FACULTY
ALL PSUs	ENGINEERING MATHS	11:00 AM	ANANT SIR
GATE 2024-25	NETWORK THEORY	6:00 PM	RAVI SIR
GATE 2024-25	ELECTRICAL MACHINE	7:30 PM	SANTAN SIR
GATE 2024-25	COMMUNICATION	9:00 PM	RENU SIR

# FREE APP CLASS SCHEDULE



## EE & ECE ENGINEERING



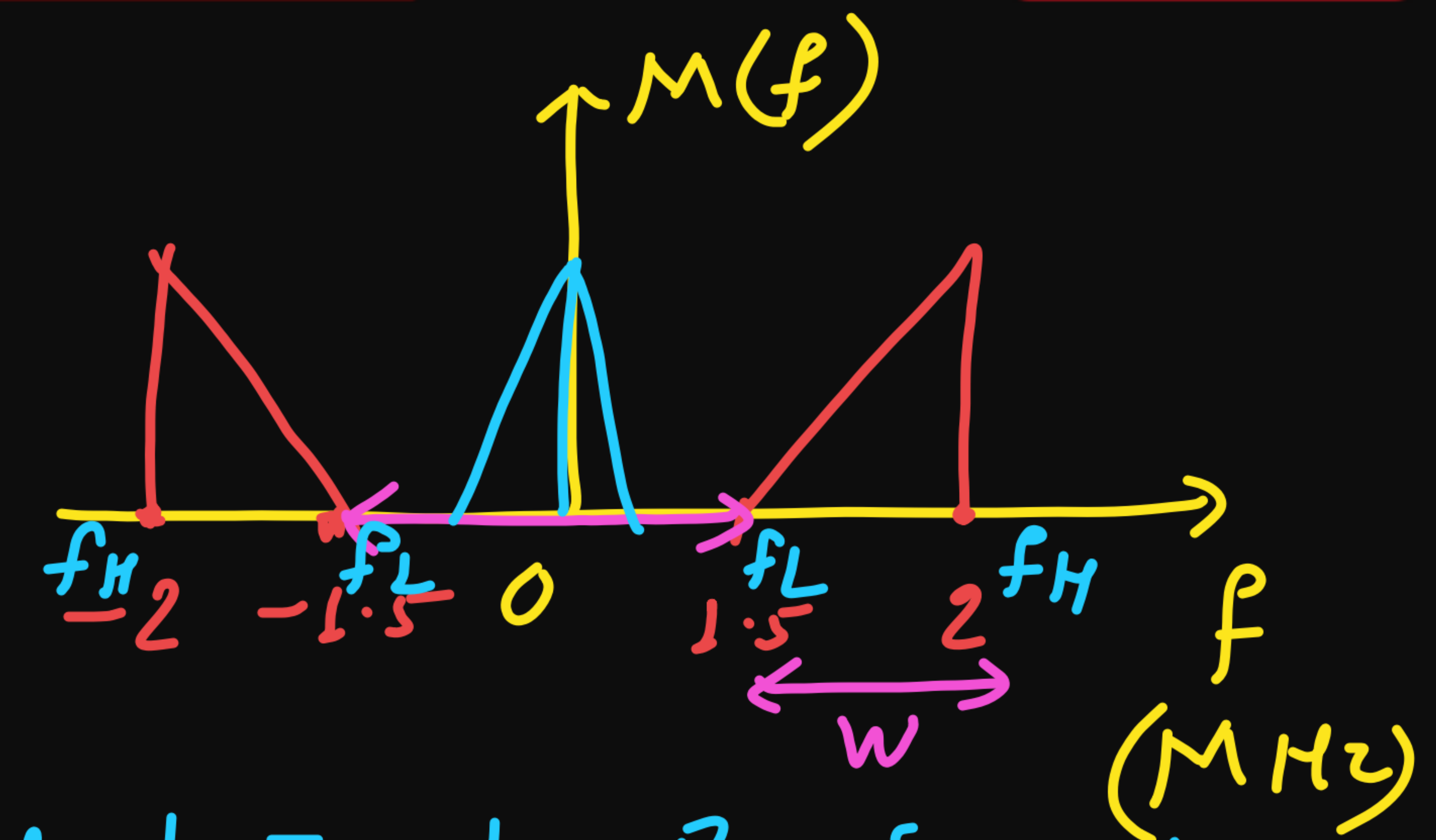
✓ NETWORK THEORY	SATURDAY Live @11AM	RAVI SIR
✓ COMMUNICATION	WEDNESDAY Live @8PM	RENU SIR
✓ ANALOG ELECTRONICS	✓ THURSDAY Live @8PM	LAWRENCE SIR
ENGINEERING MATHEMATICS	FRIDAY Live @11AM	ANANT SIR
ELECTRICAL MACHINE	✓ MONDAY Live @8PM	SANTAN SIR



[IES - EC - 2000]

A band - pass signal has significant frequency components in the range of 1.5 MHz to 2 MHz. If the signals to be reconstructed from its sample, the minimum sampling frequency will be

- ~~(A) 1 MHz~~
- (B) 2 MHz
- (C) 3.5 MHz
- (D) 4 MHz



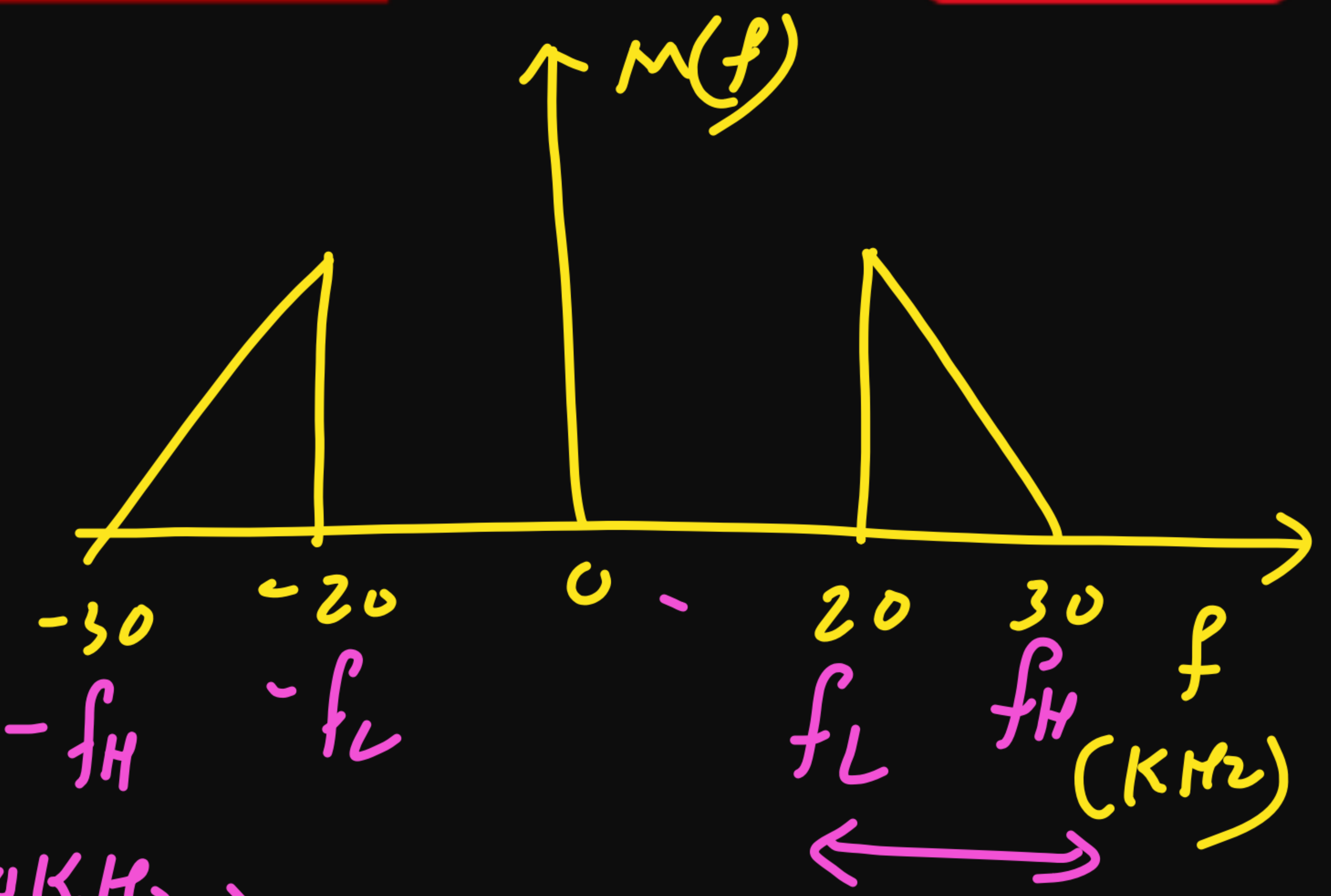
Student - I : Low Pass Signal  
 $w = 2 \text{ MHz}$   $NR = 2w = 4 \text{ MHz}$   
 $f_s \geq NR \Rightarrow (f_s)_{\min} = 4 \text{ MHz}$

Student - II : Band-Pass Signal  $\rightarrow w = 0.5 \text{ MHz}$ ,  $NR = 2w = 1 \text{ MHz}$   
 $\frac{2f_L}{2w} = \frac{3M}{1M} = 3 = m$ ,  $\frac{2f_H}{2w} = \frac{4M}{1M} = 4 = m+1$   
 $f_s = 1 \text{ MHz}$

Ans: 20 KHz

[IES - EC - 2010]

The spectrum of a band pass signal spans from 20 kHz to 30 kHz. The signal can be recovered ideally from the sampled values when the sampling rate is at least



$$\frac{2f_L}{2W} = \frac{40}{20} = 2 = m$$

$$\frac{2f_H}{2W} = \frac{60}{20} = 3 = m+1$$

$$\frac{2f_L}{m} = \frac{40k}{2} = 20k\text{Hz}$$

$$\frac{2f_H}{m+1} = \frac{60k}{3} = 20k\text{Hz}$$

$$(f_s)_{min} = 20k\text{Hz}$$

$W = 10\text{kHz}$   
 $2W = 20\text{kHz}$   
 $NR = 20\text{kHz}$

[IES - EC - 2009]

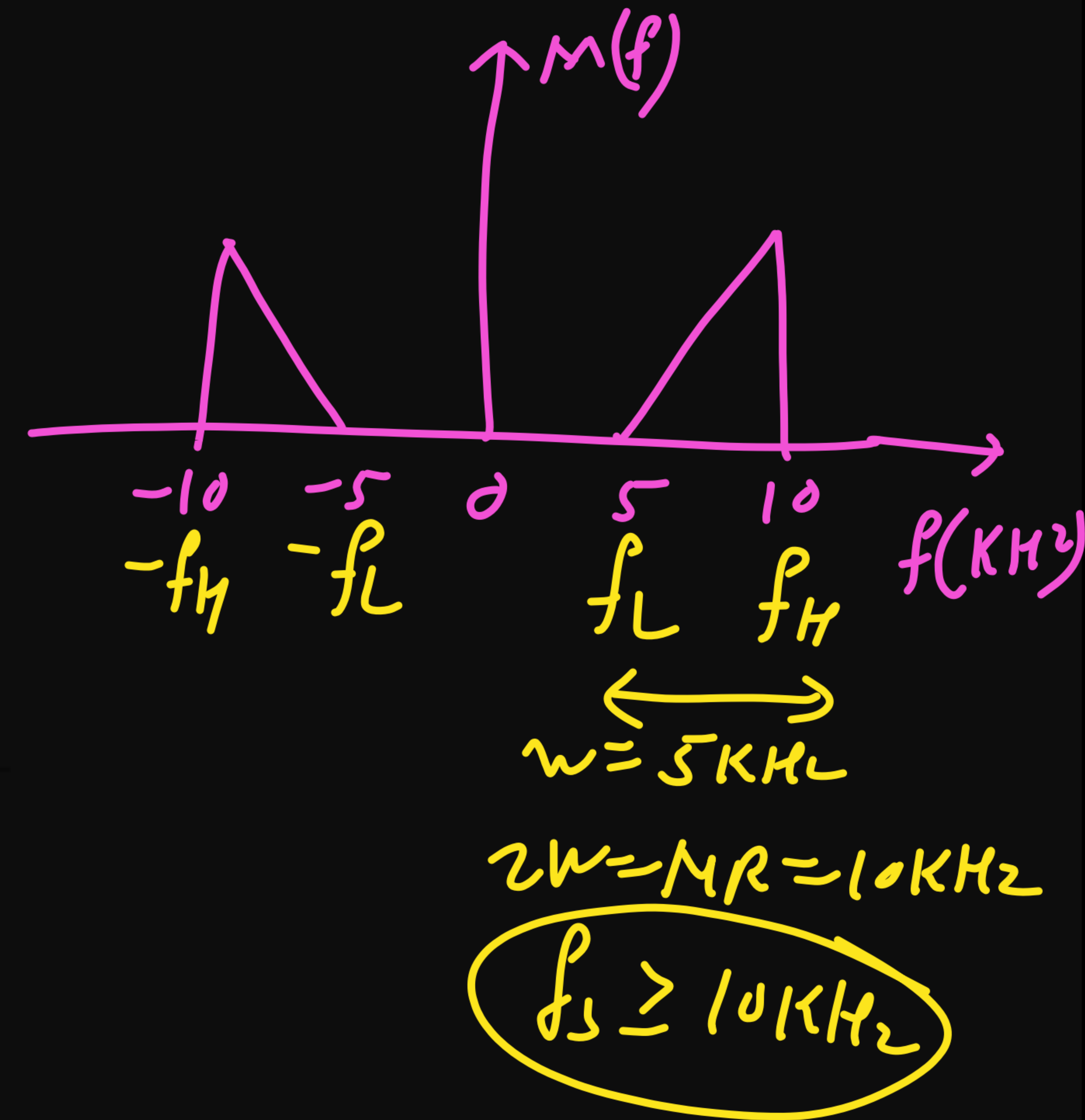
A signal occupies a band 5 kHz to 10 kHz. For proper error free reconstruction at what rate it should be sampled?

- ~~(A) 10 kHz~~
- (B) 20 kHz
- (C) 30 kHz**
- (D) 40 kHz**

$$\frac{2f_L}{2W} = \frac{10}{10} = 1 = m$$

$$\frac{2f_H}{2W} = \frac{20}{10} = 2 = m+1$$

$$f_s = 10 \text{ kHz}$$



[GATE - EC - 2007]

In delta modulation, the slope overload distortion can be reduced by

- (A) decreasing the step size  $\times$
- (B) decreasing the granular noise  $\times$
- (C) decreasing the sampling rate  $\times$
- ~~(D) increasing the step size~~

D.M.

$$\frac{\Delta}{T_s} = \left. \frac{d}{dt} m(t) \right|_{\max} = \text{No Error}$$

$$\frac{\Delta}{T_s} > \left. \frac{d}{dt} m(t) \right|_{\max} = \text{Granular Noise}$$

$$\frac{\Delta}{T_s} < \left. \frac{d}{dt} m(t) \right|_{\max} = \text{Slope overload Error}$$

To reduce slope overload error

$$\frac{\Delta}{T_s} \uparrow \Rightarrow \Delta \uparrow, T_s \downarrow, f_s \uparrow$$

[GATE - EC1 - 2015]

A sinusoidal signal of 2 kHz frequency is applied to a delta modulator. The sampling rate and step-size  $\Delta$  of the delta modulator are 20,000 samples per second and 0.1 V, respectively. To prevent slope overload, the maximum amplitude of the sinusoidal signal (in Volts) is :

- ~~(A)  $\frac{1}{2\pi}$~~
- (C)  $\frac{2}{\pi}$

- (B)  $\frac{1}{\pi}$
- (D)  $\pi$

$A_m \leq \frac{2000}{2\pi \times 2000} = \frac{1}{2\pi}$

$f_m = 2\text{kHz}$

$m(t) = A_m \sin 2\pi f_m t \rightarrow \frac{d}{dt} m(t) = A_m 2\pi f_m$   
max

$f_s = \frac{1}{T_s} = 20000 \text{ samples/sec}$

$\Delta = 0.1 \text{ Volt}$

for slope overload

$\frac{\Delta}{T_s} < \frac{d}{dt} m(t) |_{\text{max}}$

To prevent slope overload

$\frac{\Delta}{T_s} \geq \frac{d}{dt} m(t) |_{\text{max}}$

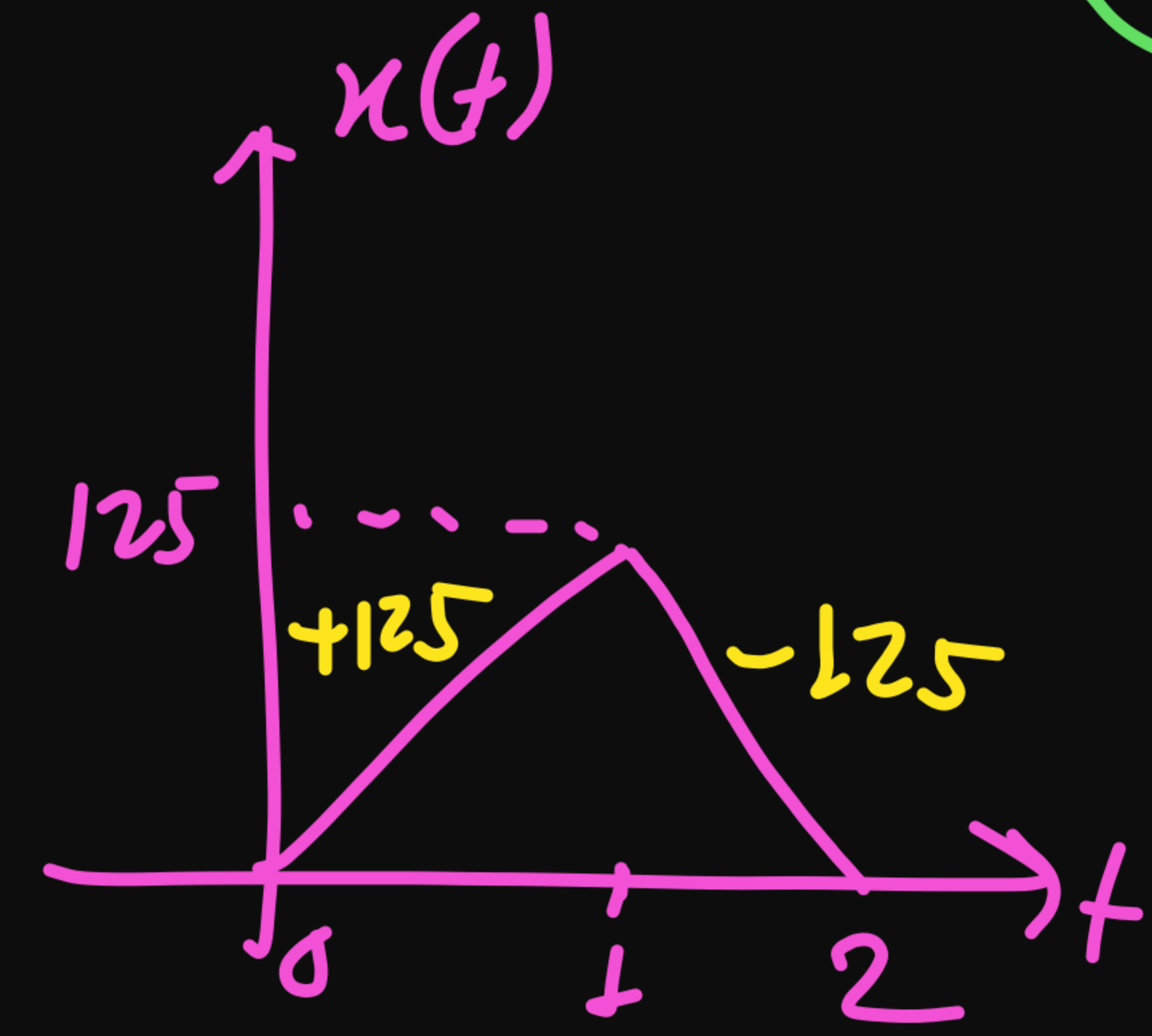
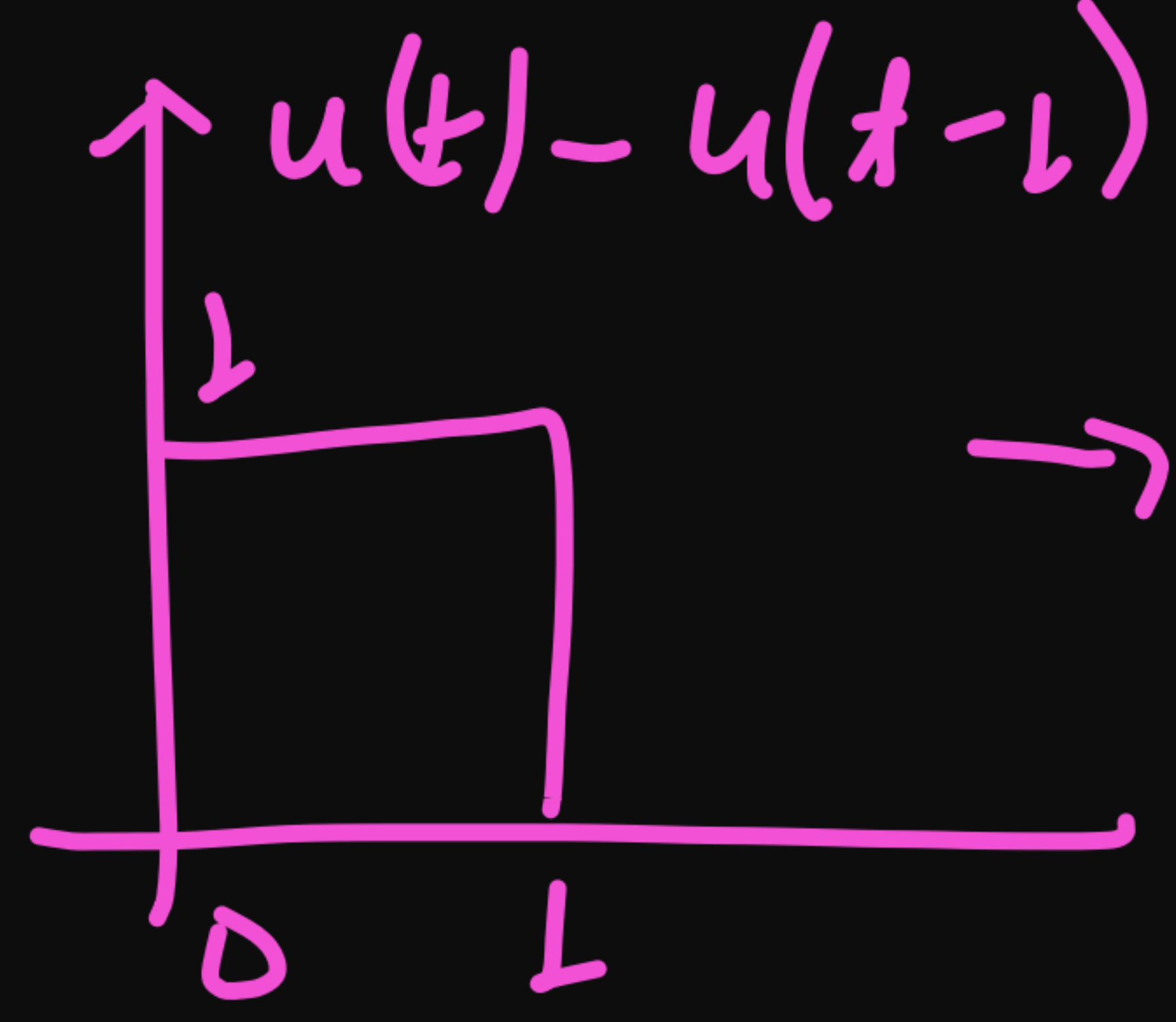
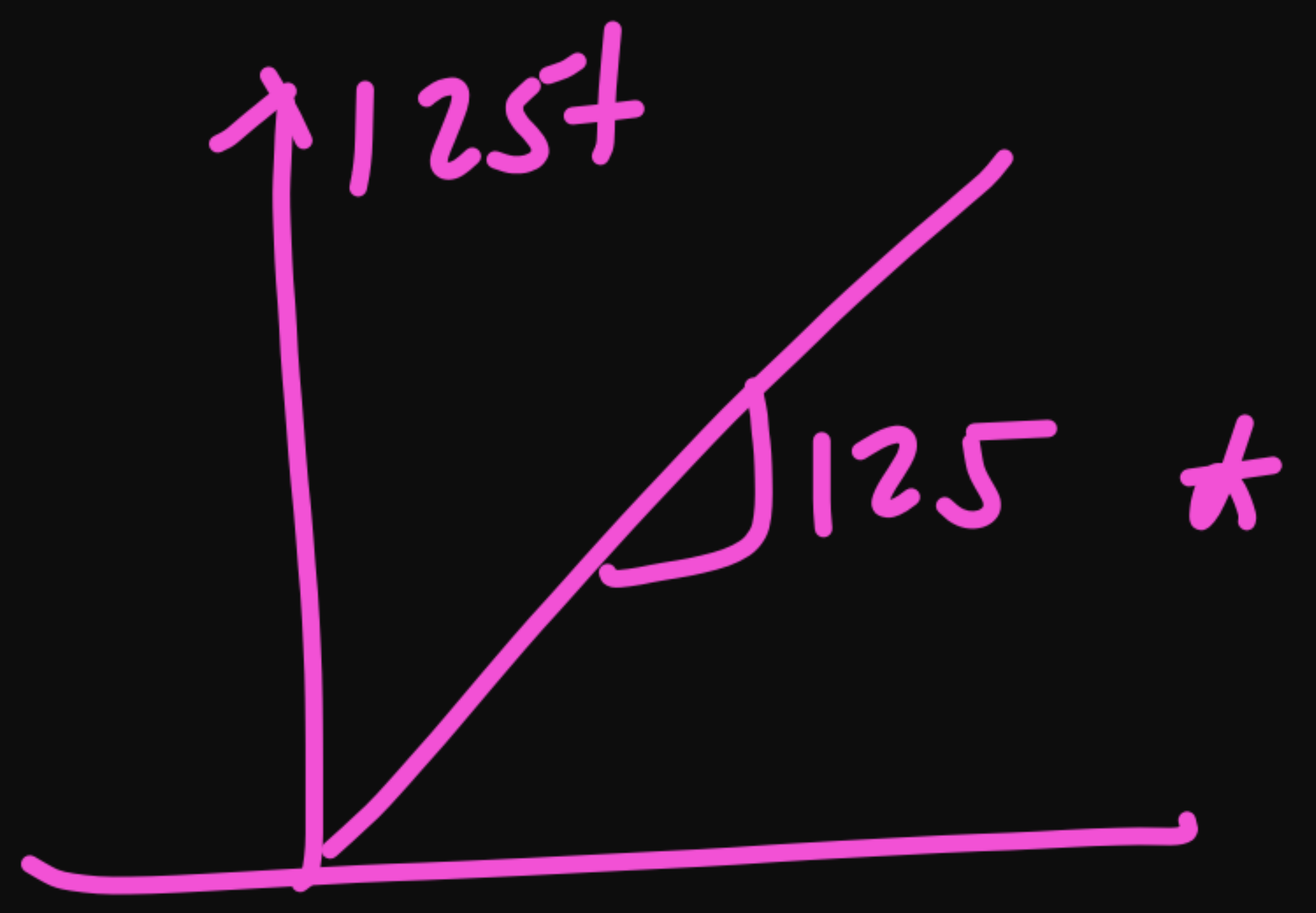
$0.1 \times 20000 \geq 2\pi f_m \cdot A_m$

[GATE - EC - 2006]

The minimum step-size required for a Delta-Modulator operating at 32K samples/sec to track the signal (here  $u(t)$  is the unit-step function)  $x(t) = 125t(u(t) - u(t - 1)) + (250 - 125t)(u(t - 1) - u(t - 2))$  so that slope-overload is avoided, would be

- (A)  $2^{-10}$
- (C)  $2^{-6}$

- ~~(B)  $2^{-8}$~~
- (D)  $2^{-4}$



$$f_s = \frac{1}{T_s} = 32000 \text{ samples/sec}$$

To avoid slope overload error

$$\frac{\Delta}{T_s} \geq \left. \frac{d m(t)}{dt} \right|_{\max}$$

$$\Delta \geq \frac{125}{32000}$$

$$\left. \frac{d m(t)}{dt} \right|_{\max} = +125$$

$$\Delta_{\min} = \frac{125}{32000}$$

$$= \frac{125}{32 \times 1000}$$

$$= \frac{1}{2^5 \times 2^3}$$

$$= 2^{-8} \checkmark$$

[GATE - EC - 2003]

The input to a linear delta modulator having a step size  $\Delta = 0.628$  is a sine wave with frequency  $f_m$  and peak amplitude  $A_m$ . If the sampling frequency  $f_s = 40\text{KHz}$ , the combination of the sine-wave frequency and the amplitude, where slope overload will take place is

	$A_m$	$f_m$
X (A)	0.3V	8 KHZ = 2.4 KHZ = 2400
✓ (B)	1.5 V	4 KHZ = 6000
X (C)	1.5 V	2 KHZ = 3000
X (D)	3.0 V	1 KHZ = 3000

$$\Delta = 0.628, f_s = \frac{1}{T_s} = 40\text{KHz} = 40000 \text{ (samples/sec)}$$

$$m(t) = A_m \sin 2\pi f_m t$$

$$\frac{\Delta}{T_s} < \left. \frac{d}{dt} m(t) \right|_{\max}$$

$$0.628 \times 40000 < 2\pi f_m A_m$$

$$f_m A_m > \frac{0.628 \times 40000}{2\pi} = \frac{0.628 \times 40\text{K}}{6.28}$$

$$f_m A_m > 4000$$

[IES - EC - 2004]

The sum of two signals

$$e_1 = 3 \sin(4\pi \times 10^3 t)$$

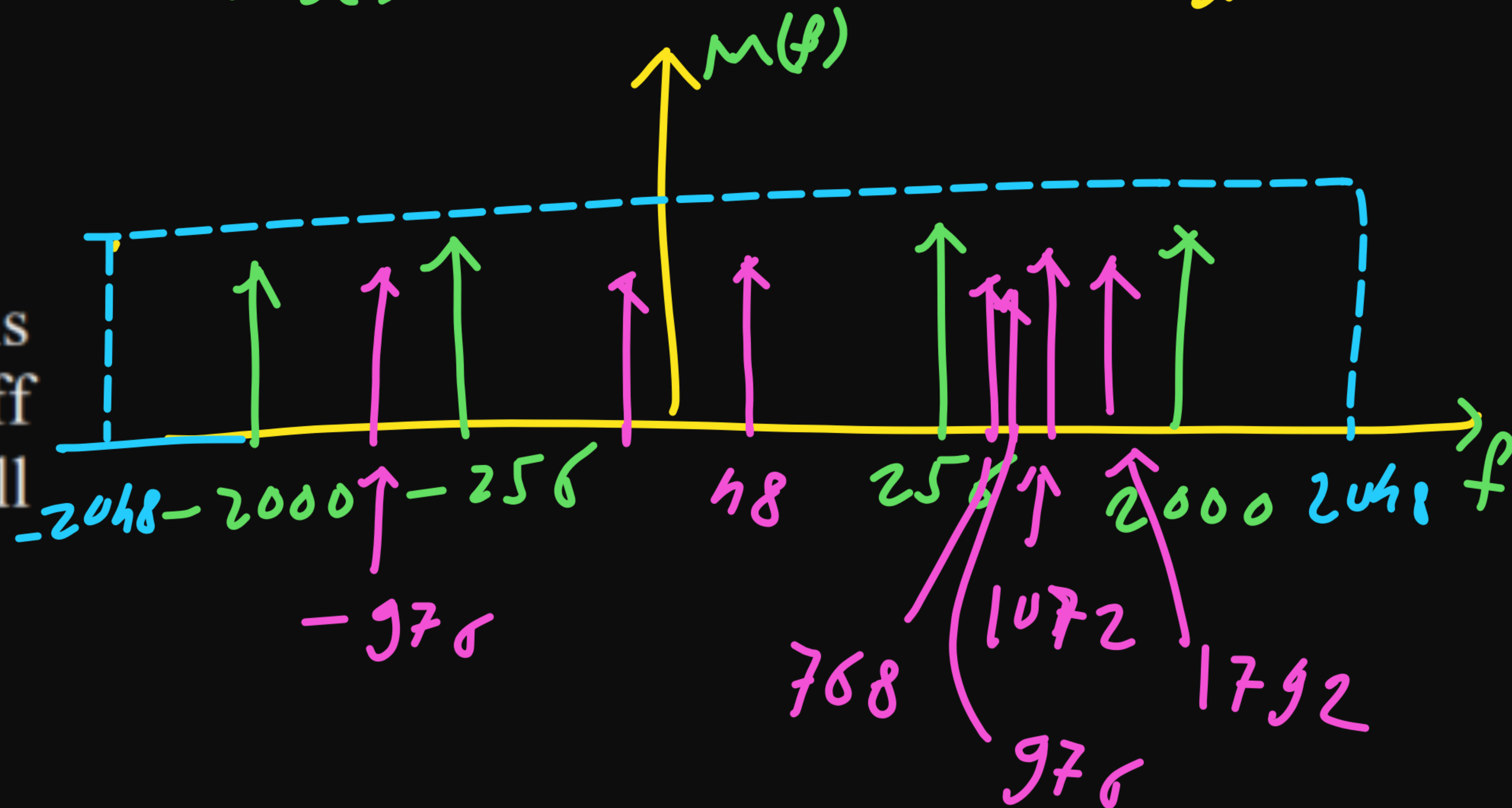
and  $e_2 = 5 \sin(2\pi \times 256 t)$

is sampled at 1024 Hz. The sampled signal is passed through a low pass filter with cut-off at 2048 Hz. The output of this filter will contain components at

- (A) 256 Hz and 1000 Hz ~~X~~
- (B) 256 Hz and 1024 Hz ~~X~~
- (C) 256 Hz only
- (D) 1024 Hz only ~~X~~

$$m_1(t) = 3 \sin 2\pi \cdot 2000 t \quad \left. \begin{array}{l} \text{Double tone} \\ \text{Sinusoid} \end{array} \right\}$$

$$m_2(t) = 5 \sin 2\pi \cdot 256 t$$





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